


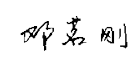
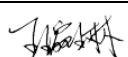
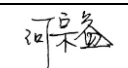
# SPECIFICATION FOR LCD MODULE

**MODULE NO: YB-TG320130C01A-N-A1**

**Doc.Version:04**

Customer Approval:

<input checked="" type="checkbox"/> Accept	<input type="checkbox"/> Reject
  2020-9-27	

YEEBO	NAME	SIGNATURE	DATE
Prepare	Electronic Engineer		2020/9/25
Check	Mechanical Engineer		2020/9/25
Verify			
Approval			2020/9/25

APPROVAL FOR SPECIFICATIONS ONLY

APPROVAL FOR SPECIFICATIONS AND SAMPLE

WIMRD005-02-D

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YEEBO	NAME	SIGNATURE	DATE
Prepare	Electronic Engineer	邱嘉剛	2020/9/25
Check	Mechanical Engineer	[Handwritten Signature]	2020/9/25
Verify			
Approval		[Handwritten Signature]	2020/9/25

APPROVAL FOR SPECIFICATIONS ONLY

APPROVAL FOR SPECIFICATIONS AND SAMPLE

WIMRD005-02-D



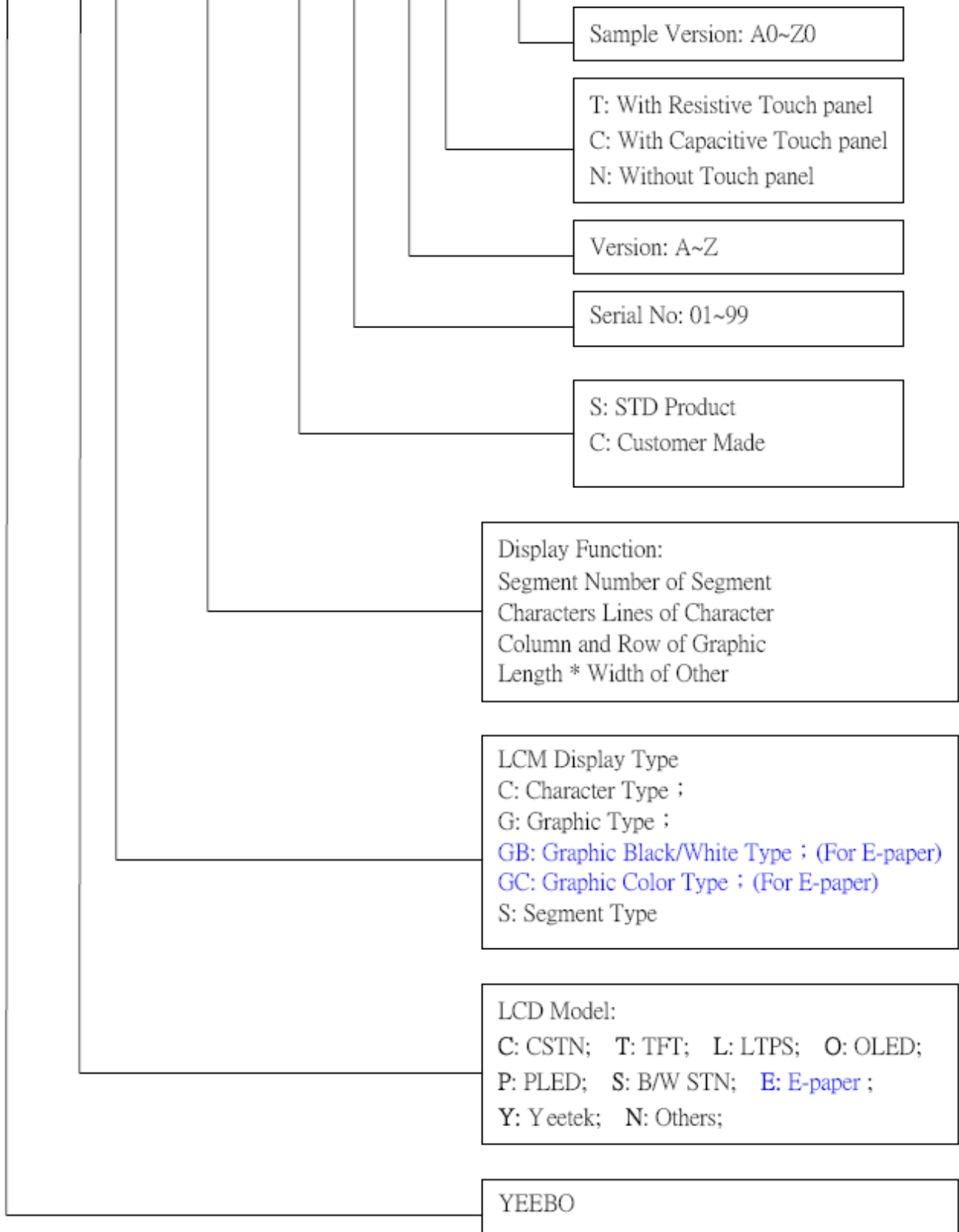
## 2. Table of Contents:

NO	CONTENTS	PAGE
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### 3. Module Numbering System:

(Example)

**YB-TG240320S01D-T-A0**

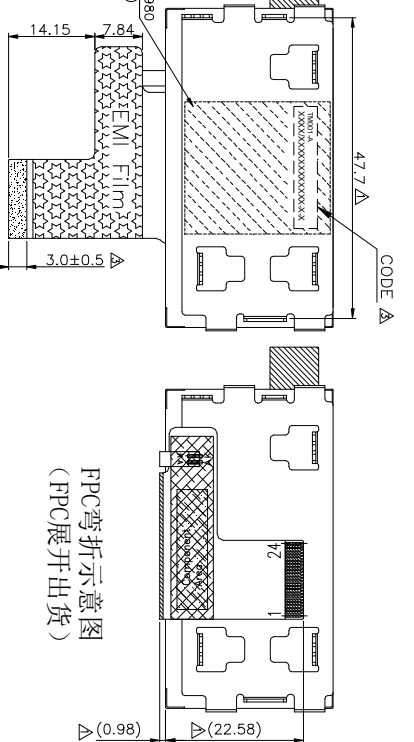
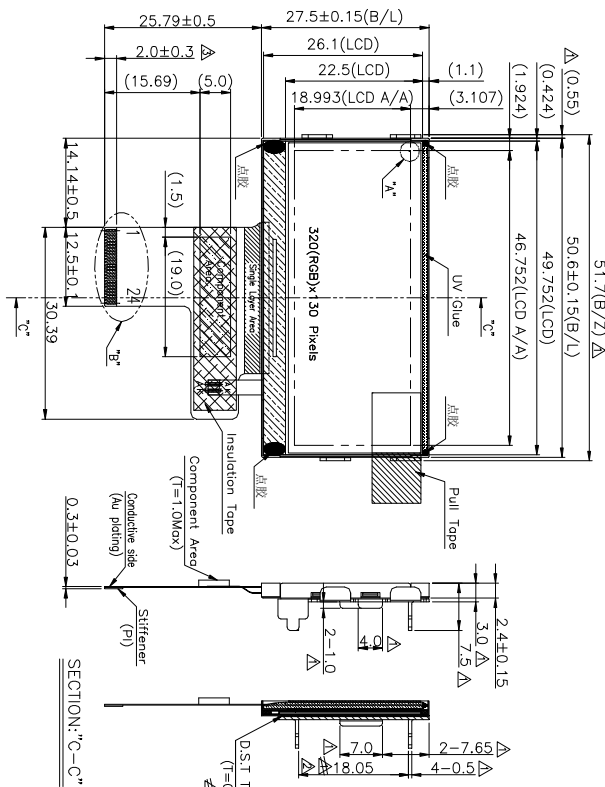
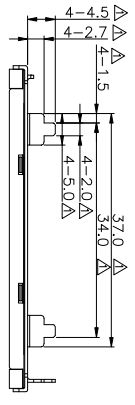


#### **4. General Specification:**

ITEM	CONTENTS
Module Size	51.7(W) * 27.5(H) * 7.5(T) mm
Module Size(With FPC)	51.7(W) * 53.29(H) * 7.5(T) mm
Display Size(Diagonal)	2.0 inch
Display Format	320(RGB)* 130 Pixels
Active Area	46.752(W) * 18.993(H) mm
Pixel Pitch	0.1461* 0.1461mm
LCD Type	TFT (262K)/ Transmissive / NW
The Best View Angle	6 O'clock
View Angle (Gray Inversion)	12 O'clock
Controller IC	ILI9342C
Weight	11.5g

### 5. LCM drawing:

Count drawing & Spec.revision record during discussion with customer	
Rev.	Revision content description
#1	FIRST ISSUE
#2	Add the iron frame
#3	Modify Drawing
#4	Modify FPC Size & Add CODE



- Specification:**
1. Display mode: TFT/ Normally White/ Transmissive
  2. Display Color: 262K
  3. Viewing direction: Gray Inversion direction: 12 O'clock  
The Best Viewing Direction: 6 O'clock
  4. Operating temperature: 20°C to +70°C
  5. Storage temperature: -30°C to +80°C
  6. Drive IC is: IL19342C or Compatible
  7. Backlight: 4 CHIP WHITE LED
  8. ROHS compliant

△ CODE NOTES:  
 TM001-A \_\_\_\_\_ Customer P/N.  
 XXXX/XXXXXXXXXXXX-X-X \_\_\_\_\_ LOT NO.  
 \_\_\_\_\_ Production Year and week

<b>YEEBO</b>		MOD. Name	YB-TG320130C01A-N-A
UNIT	SIZE	DESIGNED	王琬琳
mm	A4	CHECKED	
SCALE	N-T-S	VERIFIED	
		APPROVED	
		FILE NAME	Count Dwg.

NO	FPC PIN OUT	Sheet	1
1	VCCIO (1.8V)	Of	1
2	VCI (2.8V)		
3	GND		
4	LED+		
5	LED-		
6	LED-		
7	LED-		
8	RESX		
9	GND		
10	CSX		
11	WRX		
12	RDX		
13	D/CX		
14	GND		
15	DB0		
16	DB1		
17	DB2		
18	DB3		
19	DB4		
20	DB5		
21	DB6		
22	DB7		
23	DB8		
24	SEL9/8BIT		

## 6. Electrical Characteristics

### 6-1 Absolute Maximum Ratings

(Ta=25°C GND=0V)

Item	Symbol	Min.	Type	Max.	Unit	Remark
Supply Voltage(Logic)	VCI	-0.3	-	4.2	Volt	-
Supply Voltage(Digital)	VCCIO	-0.3	-	3.0	Volt	-
Operating Temperature	Topr	-20	-	+70	°C	-
Storage Temperature	Tstg	-30	-	+80	°C	-

Note1: Absolute maximum rating is the limit value beyond which the IC maybe broken.  
They do not assure operations.

### 6-2 Operating Conditions

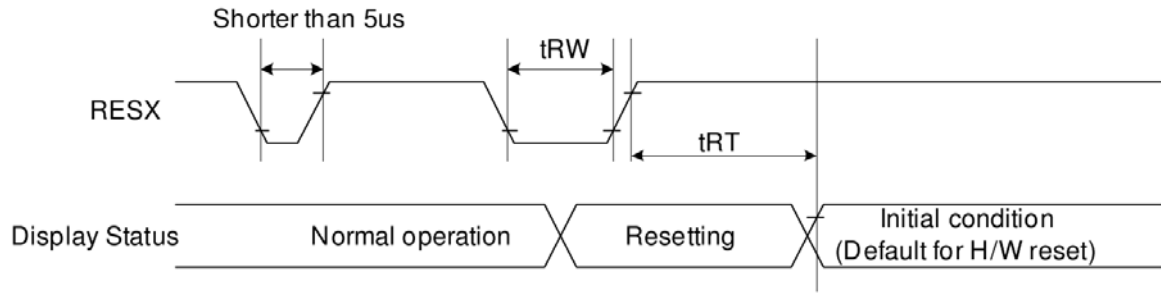
(Ta=25°C )

Item	Symbol	Condition	Min.	Typ.	Max.	Unit
Supply Voltage	VDD	-	2.6	2.8	3.3	Volt
Normal mode Current consumption	IDD	-	-	3.2	4.8	mA
Logic high level input voltage	VIH	-	0.7*VCCIO	-	VCCIO	V
Logic low level input voltage	VIL	-	GND	-	0.3*VCCIO	V



### 6-3 Timing Characteristics

#### Reset timing



Signal	Symbol	Parameter	Min	Max	Unit
RESX	tRW	Reset pulse duration	10		uS
	tRT	Reset cancel		5 (note 1,5)	mS
				120 (note 1,6,7)	mS

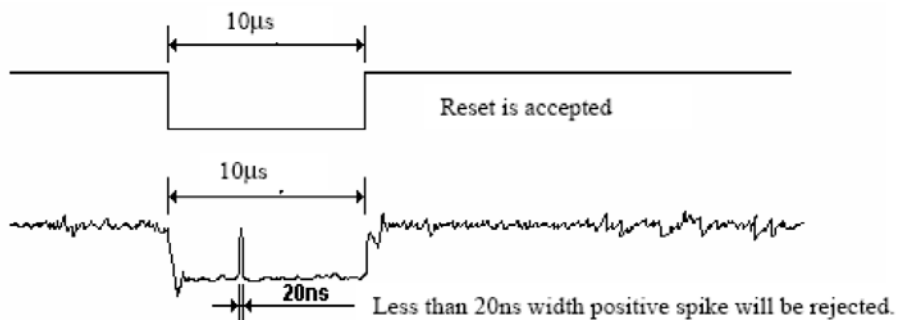
Note 1: The reset cancel includes also required time for loading ID bytes, VCOM setting and other settings from NV memory to registers. This loading is done every time when there is HW reset cancel time (tRT) within 5 ms after a rising edge of RESX.

Note 2: Spike due to an electrostatic discharge on RESX line does not cause irregular system reset according to the table below: -

RESX Pulse	Action
Shorter than 5us	Reset Rejected
Longer than 10us	Reset
Between 5us and 10us	Reset starts

Note 3: During the Resetting period, the display will be blanked (The display is entering blanking sequence, which maximum time is 120 ms, when Reset Starts in Sleep Out -mode. The display remains the blank state in Sleep In -mode.) And then return to Default condition for Hardware Reset.

Note 4: Spike Rejection also applies during a valid reset pulse as shown below:

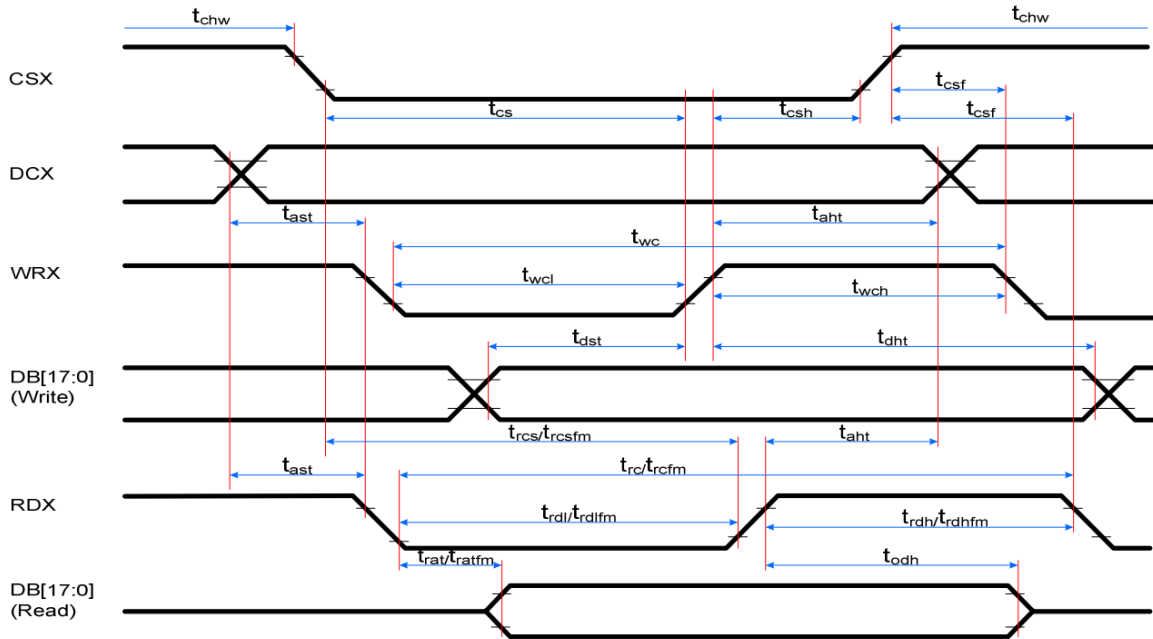


Note 5: When Reset applied during Sleep In Mode.

Note 6: When Reset applied during Sleep Out Mode.

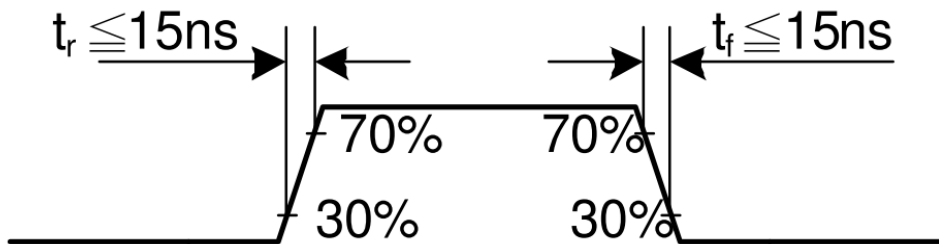
Note 7: It is necessary to wait 5msec after releasing RESX before sending commands. Also Sleep Out command cannot be sent for 120msec.

## Display Parallel 8-bit interface timing characteristics(8080-I system)

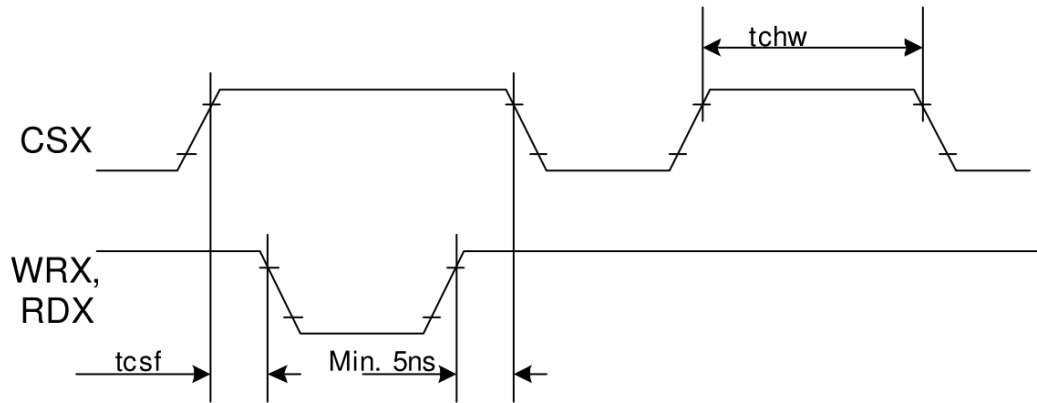


Signal	Symbol	Parameter	min	max	Unit	Description
DCX	t <sub>ast</sub>	Address setup time	0	-	ns	
	t <sub>ah</sub>	Address hold time (Write/Read)	10	-	ns	
CSX	t <sub>chw</sub>	CSX "H" pulse width	0	-	ns	
	t <sub>csc</sub>	Chip Select setup time (Write)	15	-	ns	
	t <sub>rcs</sub>	Chip Select setup time (Read ID)	45	-	ns	
	t <sub>rcsfm</sub>	Chip Select setup time (Read FM)	355	-	ns	
	t <sub>csf</sub>	Chip Select Wait time (Write/Read)	10	-	ns	
WRX	t <sub>wc</sub>	Write cycle	66	-	ns	
	t <sub>wrh</sub>	Write Control pulse H duration	15	-	ns	
	t <sub>wrl</sub>	Write Control pulse L duration	15	-	ns	
RDX (FM)	t <sub>rcfm</sub>	Read Cycle (FM)	450	-	ns	
	t <sub>rdhfm</sub>	Read Control H duration (FM)	90	-	ns	
	t <sub>rdlfm</sub>	Read Control L duration (FM)	355	-	ns	
RDX (ID)	t <sub>rc</sub>	Read cycle (ID)	160	-	ns	
	t <sub>rdh</sub>	Read Control pulse H duration	90	-	ns	
	t <sub>rdl</sub>	Read Control pulse L duration	45	-	ns	
D[17:0], D[15:0], D[8:0], D[7:0]	t <sub>dst</sub>	Write data setup time	10	-	ns	For maximum CL=30pF For minimum CL=8pF
	t <sub>dht</sub>	Write data hold time	10	-	ns	
	t <sub>rat</sub>	Read access time	-	40	ns	
	t <sub>ratfm</sub>	Read access time	-	340	ns	
	t <sub>rod</sub>	Read output disable time	20	80	ns	

Note:  $T_a = -30$  to  $70$  °C,  $IOVCC=1.65V$  to  $2.8V$ ,  $VCI=2.6V$  to  $3.3V$ ,  $GND=0V$

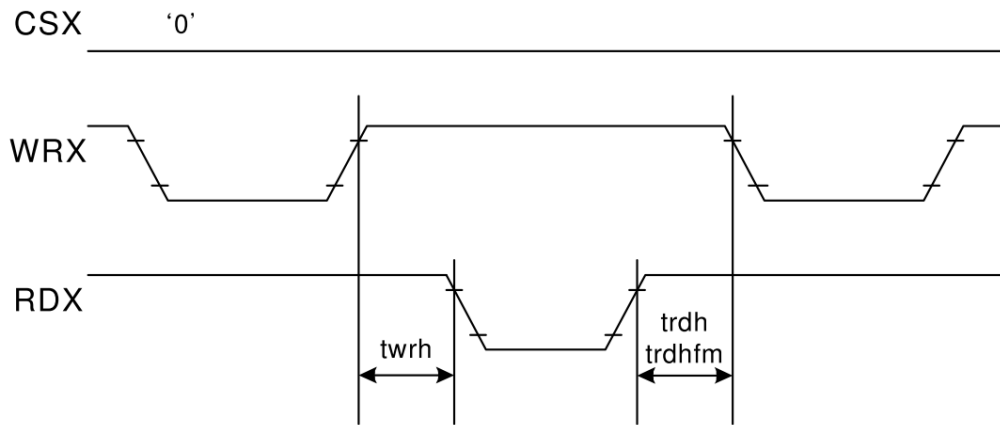


CSX timings :



Note: Logic high and low levels are specified as 30% and 70% of IOVCC for Input signals.

Write to read or read to write timings:



Note: Logic high and low levels are specified as 30% and 70% of IOVCC for Input signals.

## 7. Optical Characteristics:

Item	Symbol	Conditions	Specifications			Unit	Note	
			Min	Typ	Max			
Transmittance (Without POL)	T(%)	-	-	15.2	-	-	-	
Contrast Ratio	CR	$\Theta=0$ Normal Viewing Angle	400	500	-	-	(1) (2)	
Response Time	TR+TF	-	-	16	28	ms	(1) (3)	
Viewing angle	Hor	$\Theta_{x+}$	CR $\geq$ 10	-	70	-	deg.	-
		$\Theta_{x-}$		-	70	-		
	Ver	$\Theta_{y+}$		-	70	-		
		$\Theta_{y-}$		-	60	-		

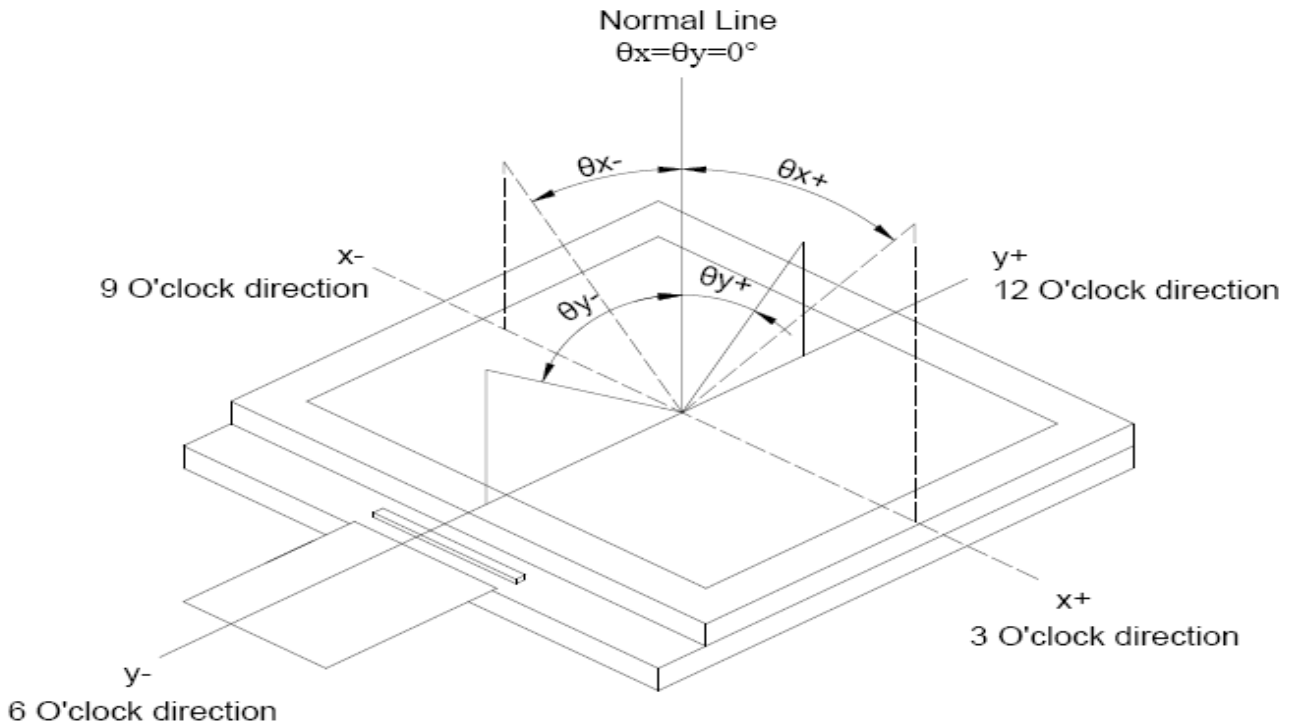
### Measuring Condition

1. Measuring surrounding: dark room
2. Ambient temperature: 25 $\pm$ 2 $^{\circ}$ C
3. 30 min. Warm-up time.

### Color of CIE Coordinate:

Item		Symbol	Condition	Min.	Typ.	Max.
Chromaticity Coordinates (Transmissive)	Red	x	$\theta = \phi = 0^{\circ}$ Backlight Color Degree	0.5576	0.6076	0.6576
		y		0.2885	0.3385	0.3885
	Green	x		0.2964	0.3464	0.3964
		y		0.5046	0.5546	0.6046
	Blue	x		0.1053	0.1553	0.2053
		y		0.0525	0.1025	0.1525
	White	x		0.2631	0.3131	0.3631
		y		0.2677	0.3177	0.3677

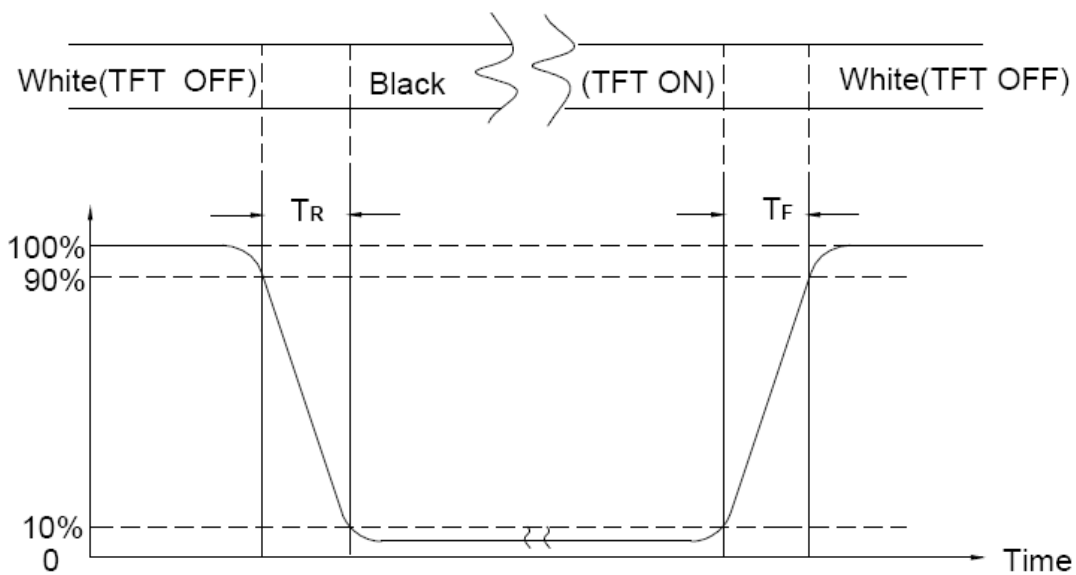
Note (1) Definition of Viewing Angle :



Note (2) Definition of Contrast Ratio(CR) :  
measured at the center point of panel

$$\text{Contrast ratio (CR)} = \frac{\text{Photo detector output when LCD is at "White" state}}{\text{Photo detector output when LCD is at "Black" state}}$$

Note (3) Definition of Response Time : Sum of  $T_R$  and  $T_F$



## **8. Interface Pin Assignment:**

No.	Symbol	Function
1	VCCIO	Power supply(Digital)
2	VCI	Power supply(Logic)
3	GND	Ground
4	LED+	LED anode
5		
6	LED-	LED cathode
7		
8	RESX	Reset signal
9	GND	Ground
10	CSX	Chip select signal
11	WRX	Write enable signal
12	RDX	Read enable signal
13	D/CX	Display data(D/CX=1) / Command selection(D/CX=0)
14	GND	Ground
15~23	DB0~DB8	Data bus
24	SEL9/8BIT	Interface seletion

## 9. Backlight:

### 1. Standard Lamp Styles (Edge Lighting Type):

The LED chips are distributed over the edge light area of the illumination unit, which gives the less power consumption:

### 2. The Main Advantages of the LED Backlight are as following:

- 2.1 The brightness of the backlight can simply be adjusted.  
By a resistor or a potentiometer.

### 3. Data About LED Backlight:

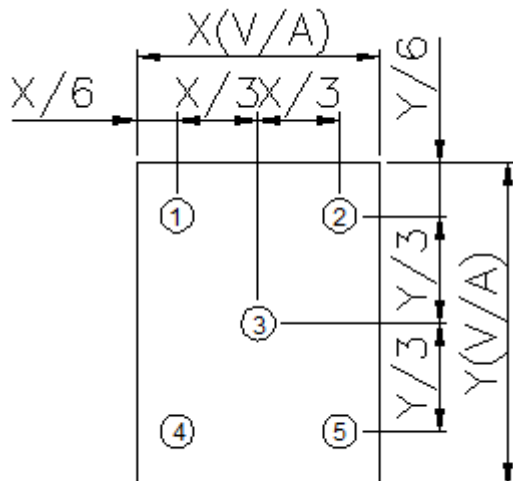
(Ta=25°C)

PARAMETER	Sym.	Min.	Typ.	Max.	Unit	Test Condition	Note
Supply Current	I	-	60	-	mA	-	-
Supply Voltage	V	2.7	3.1	3.5	V	If=60mA	-
Luminous Intensity for LCM	IV	250	300	-	cd/m <sup>2</sup>		2
Uniformity for LCM	-	70	-	-	%		3
Life Time	-	20000	-	-	Hr.		4
Color	White						

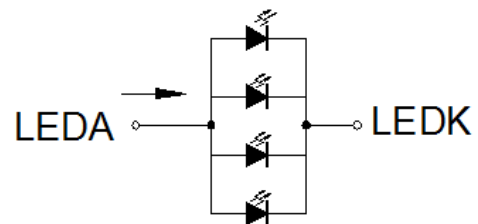
NOTE:

- Backlight Only
- Average Luminous Intensity of P1-P5
- Uniformity = Min/Max \* 100%
- LED life time defined as follows: The final brightness is at 50% of original brightness

#### Measured Method: (X\*Y: Light Area)



#### Internal Circuit Diagram



#### (Effective spatial Distribution)

Using aperture of 0.1°, distance 50cm.

## 10. Standard Specification for Reliability:

### 10-1. Standard Specifications for Reliability of LCD Module

No	Item	Description
01	High temperature operation	The sample should be allowed to stand at 70°C for 120 hours under driving condition and then returning it to normal temperature condition, and allowing it stand for 2 hours.
02	Low temperature operation	The sample should be allowed to stand at -20°C for 120 hours under driving condition and then returning it to normal temperature condition, and allowing it stand for 2 hours.
03	High temperature storage	The sample should be allowed to stand at 80°C for 240 hours under no-load condition, and then returning it to normal temperature condition, and allowing it stand for 2 hours.
04	Low temperature storage	The sample should be allowed to stand at -30°C for 240 hours under no-load condition, then returning it to normal temperature condition, and allowing it stand for 2 hours.
05	Moisture storage	The sample should be allowed to stand at 60°C,90%RH MAX for 240 hours under no-load condition, then taking it out and drying it at normal temperature for 2 hours.
06	Thermal shock storage	The sample should be allowed to stand the following 10 cycles : -30°C for 30 minutes → normal temperature for 5 minutes → +80°C for 30 minutes → normal temperature for 5 minutes, as one cycle.
07	Packing vibration	Frequency range : 10Hz ~ 55Hz Amplitude of vibration : 1.5mm      Sweep time: 12 min X,Y,Z 2 hours for each direction.
08	Packing drop test	According to ISTA 1A 2001.
09	Electrical Static Discharge	Air: ±4KV 150pF/330Ω 5 times
		Contact: ±2KV 150pF/330Ω 5 time

\*Sample size for each test item is 3~5pcs



## 10 - 2. Testing Conditions and Inspection Criteria

For the final test the testing sample must be stored at room temperature for 24 hours, after the tests listed in Table 10-1, Standard specifications for Reliability have been executed in order to ensure stability.

No	Item	Test Model	In section Criteria
01	Current Consumption	Refer To Specification	The current consumption should conform to the product specification.
02	Contrast	Refer To Specification	After the tests have been executed, the contrast must be larger than half of its initial value prior to the tests.
03	Appearance	Visual inspection	Defect free.

## 10- 3. MTBF

MTBF	Functions, performance, appearance, etc. shall be free from remarkable deterioration within 50,000 hours under ordinary operating and storage conditions room temperature ( $25\pm 5^{\circ}\text{C}$ ), normal humidity ( $50\pm 10\%$ RH), and in area not exposed to direct sun light.
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## **11. Specification of Quality Assurance:**

### 11-1. Purpose

This standard for Quality Assurance should affirm the quality of LCD module products to supply to purchaser by YEEBO CORPORATION (Supplier).

### 11-2. Standard for Quality Test

#### a. Inspection:

Before delivering, the supplier should take the following tests, and affirm the quality of product.

#### b. Electro-Optical Characteristics:

According to the individual specification to test the product.

#### c. Test of Appearance Characteristics:

According to the individual specification to test the product.

#### d. Test of Reliability Characteristics:

According to the definition of reliability on the specification for testing products.

#### e. Delivery Test:

Before delivering, the supplier should take the delivery test.

(i) Test method: According to ISO2859-1. General Inspection Level II take a single time.

(ii) The defects classify of AQL as following:

Major defect: AQL = 0.65

Minor defect: AQL = 2.5

Total defects: AQL = 2.5

### 11-3. Non- conforming Analysis & Deal With Manners

#### a. Non- conforming Analysis:

(i) Purchaser should supply the detail data of non- conforming sample and the non-conforming.

(ii) After accepting the detail data from purchaser, the analysis of non- conforming should be finished in two weeks.

(iii) If supplier can not finish analysis on time, must announce purchaser before 3 days.

#### b. Disposition of non- conforming:

(i) If find any product defect of supplier during assembly time, supplier must change the good product for every defect after recognition.

(ii) Both supplier and customer should analyze the reason and discuss the disposition of non- conforming when the reason of nonconforming is not sure.

### 11-4. Agreement items

Both sides should discuss together when the following problems happen.

a. There is any problem of standard of quality assurance, and both sides should think that must be modified.

b. There is any argument item which does not record in the standard of quality assurance.

c. Any other special problem.

11-5. Standard of The Product Appearance Test

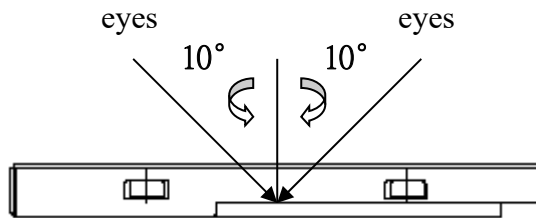
a. Manner of appearance test:

(i) The test must be under 20W × 2 or 40W fluorescent light, and the distance of view must be at 30±5cm.

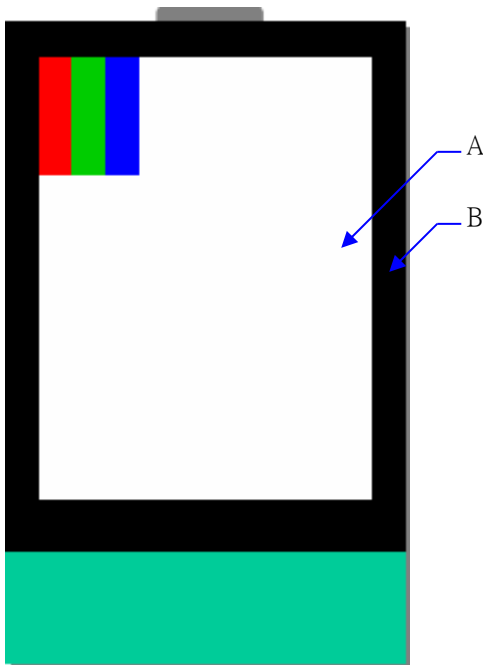
(ii) When test the model of transmissive product must add the reflective plate.

(iii) The test direction is base on around 10° of vertical line.

(iii) Temperature: 25±5°C Humidity: 60±10%RH



(iv) Definition of area:



A. Area: Viewing area.

B. Area: Out of viewing area.

(Outside viewing area)

b. Basic principle:

(i) It will accord to the AQL when the standard can not be described.

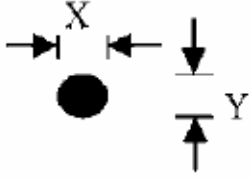
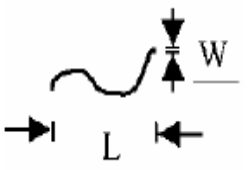
(ii) The sample of the lowest acceptable quality level must be discussed by both supplier and customer when any dispute happened.

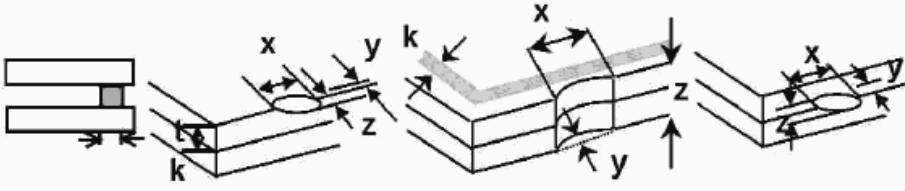
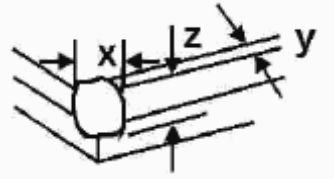
(iii) Must add new item on time when it is necessary.

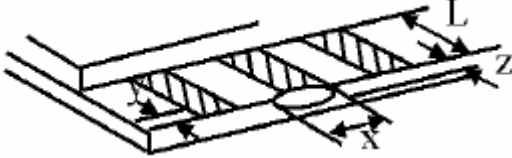
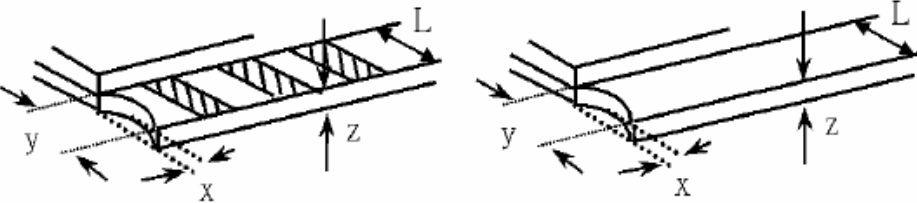
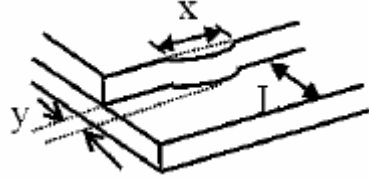
c. Standard of inspection: (Unit: mm)

11-6. Inspection specification

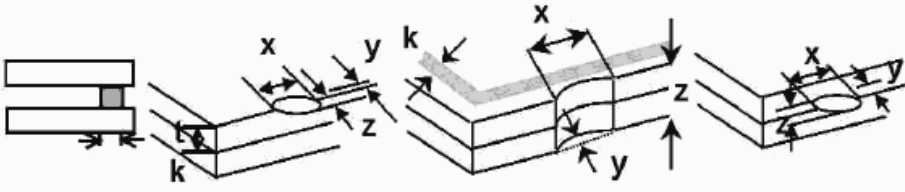
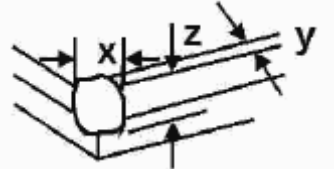
Defect out of viewing area can be neglected.

NO	Item	Criterion	AQL												
01	Electrical Testing	1.1 Missing vertical, horizontal segment, segment contrast defect. 1.2 Missing character, dot or icon. 1.3 Display malfunction. 1.4 No function or no display. 1.5 Current consumption exceeds product specifications. 1.6 LCD viewing angle defect. 1.7 Mixed product types. 1.8 Flicker	0.65												
02	Black or White spots or Bright spots or Color spots on LCD (Display only)	2.1 White and black or color spots on display $\leq 0.25\text{mm}$ , no more than Five spots. 2.2 Densely spaced: No more than three spots within 3mm. 2.3 Not visible through 5% ND filter	2.5												
03	LCD and Touch Panel black spots, white spots, contamination (non – display)	3.1 Round type: As following drawing $\Phi = (X+Y) / 2$  <table border="1" data-bbox="758 990 1292 1243"> <thead> <tr> <th>Size(mm)</th> <th>Acceptable Q'ty</th> </tr> </thead> <tbody> <tr> <td><math>\Phi \leq 0.10</math></td> <td>Accept no dense</td> </tr> <tr> <td><math>0.10 &lt; \Phi \leq 0.20</math></td> <td>2</td> </tr> <tr> <td><math>0.20 &lt; \Phi \leq 0.25</math></td> <td>2</td> </tr> <tr> <td><math>0.25 &lt; \Phi \leq 0.30</math></td> <td>1</td> </tr> <tr> <td><math>0.30 &lt; \Phi</math></td> <td>0</td> </tr> </tbody> </table> <p>* Densely spaced: No more than two spots within 3mm.</p>	Size(mm)	Acceptable Q'ty	$\Phi \leq 0.10$	Accept no dense	$0.10 < \Phi \leq 0.20$	2	$0.20 < \Phi \leq 0.25$	2	$0.25 < \Phi \leq 0.30$	1	$0.30 < \Phi$	0	2.5
		Size(mm)	Acceptable Q'ty												
$\Phi \leq 0.10$	Accept no dense														
$0.10 < \Phi \leq 0.20$	2														
$0.20 < \Phi \leq 0.25$	2														
$0.25 < \Phi \leq 0.30$	1														
$0.30 < \Phi$	0														
3.2 Line type: (As following drawing)  <table border="1" data-bbox="662 1393 1292 1668"> <thead> <tr> <th>Length(mm)</th> <th>Width(mm)</th> <th>Acceptable Q'ty</th> </tr> </thead> <tbody> <tr> <td>---</td> <td><math>W \leq 0.02</math></td> <td>Accept no dense</td> </tr> <tr> <td><math>L \leq 3.0</math></td> <td><math>0.02 &lt; W \leq 0.05</math></td> <td rowspan="2">2</td> </tr> <tr> <td><math>L \leq 2.5</math></td> <td><math>0.03 &lt; W \leq 0.08</math></td> </tr> <tr> <td>---</td> <td><math>0.08 &lt; W</math></td> <td>Rejection</td> </tr> </tbody> </table> <p>* Densely spaced: No more than two lines within 3mm.</p>	Length(mm)	Width(mm)	Acceptable Q'ty	---	$W \leq 0.02$	Accept no dense	$L \leq 3.0$	$0.02 < W \leq 0.05$	2	$L \leq 2.5$	$0.03 < W \leq 0.08$	---	$0.08 < W$	Rejection	2.5
Length(mm)	Width(mm)	Acceptable Q'ty													
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$L \leq 2.5$	$0.03 < W \leq 0.08$														
---	$0.08 < W$	Rejection													

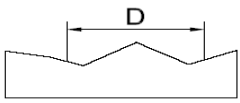
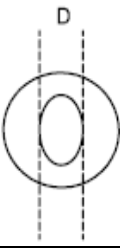
NO	Item	Criterion	AQL																		
04	Polarizer bubbles	<p>If bubbles are visible, judge using black spot specifications, not easy to find, must check in specify direction</p> <table border="1"> <thead> <tr> <th>Size <math>\Phi</math>(mm)</th> <th>Acceptable Q'ty</th> </tr> </thead> <tbody> <tr> <td><math>\Phi \leq 0.20</math></td> <td>Accept no dense</td> </tr> <tr> <td><math>0.20 &lt; \Phi \leq 0.50</math></td> <td>3</td> </tr> <tr> <td><math>0.50 &lt; \Phi \leq 1.00</math></td> <td>2</td> </tr> <tr> <td><math>1.00 &lt; \Phi</math></td> <td>0</td> </tr> <tr> <td>Total Q'ty</td> <td>3</td> </tr> </tbody> </table>	Size $\Phi$ (mm)	Acceptable Q'ty	$\Phi \leq 0.20$	Accept no dense	$0.20 < \Phi \leq 0.50$	3	$0.50 < \Phi \leq 1.00$	2	$1.00 < \Phi$	0	Total Q'ty	3	2.5						
Size $\Phi$ (mm)	Acceptable Q'ty																				
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$1.00 < \Phi$	0																				
Total Q'ty	3																				
05	Scratches	Follow NO.3 -2 Line Type.																			
06	Mura	Not visible through 5% ND filter in 50% gray.	2.5																		
07	Chipped glass	<p>Symbols:            x: Chip length            y: Chip width            z: Chip thickness            k: Seal width            t: Glass thickness    a: LCD side length            L: Electrode pad length</p> <p>7.1 General glass chip:            7.1.1 Chip on panel surface and crack between panels:</p>  <table border="1"> <thead> <tr> <th>z: Chip thickness</th> <th>y: Chip width</th> <th>x: Chip length</th> </tr> </thead> <tbody> <tr> <td><math>Z \leq 1/2t</math></td> <td>Not over viewing area</td> <td><math>x \leq 1/8a</math></td> </tr> <tr> <td><math>1/2t &lt; z \leq 2t</math></td> <td>Not exceed 1/3k</td> <td><math>x \leq 1/8a</math></td> </tr> </tbody> </table> <p>⊙ Unit: mm            ⊙ If there are 2 or more chips, x is the total length of each chip</p> <p>7.1.2 Corner crack:</p>  <table border="1"> <thead> <tr> <th>z: Chip thickness</th> <th>y: Chip width</th> <th>x: Chip length</th> </tr> </thead> <tbody> <tr> <td><math>Z \leq 1/2t</math></td> <td>Not over viewing area</td> <td><math>x \leq 1/8a</math></td> </tr> <tr> <td><math>1/2t &lt; z \leq 2t</math></td> <td>Not exceed 1/3k</td> <td><math>x \leq 1/8a</math></td> </tr> </tbody> </table> <p>⊙ Unit: mm            ⊙ If there are 2 or more chips, x is the total length of each chip</p>	z: Chip thickness	y: Chip width	x: Chip length	$Z \leq 1/2t$	Not over viewing area	$x \leq 1/8a$	$1/2t < z \leq 2t$	Not exceed 1/3k	$x \leq 1/8a$	z: Chip thickness	y: Chip width	x: Chip length	$Z \leq 1/2t$	Not over viewing area	$x \leq 1/8a$	$1/2t < z \leq 2t$	Not exceed 1/3k	$x \leq 1/8a$	2.5
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NO	Item	Criterion	AQL																
08	Glass crack	<p>Symbols:            x: Chip length            y: Chip width            z: Chip thickness            k: Seal width            t: Glass thickness    a: LCD side length            L: Electrode pad length</p> <p>8.1 Protrusion over terminal:            8.1.1 Chip on electrode pad:</p>  <table border="1" data-bbox="485 685 1171 842"> <tr> <td>y: Chip width</td> <td>x: Chip length</td> <td>z: Chip thickness</td> </tr> <tr> <td><math>y \leq 0.5\text{mm}</math></td> <td><math>x \leq 1/8a</math></td> <td><math>0 &lt; z \leq t</math></td> </tr> </table> <p>8.1.2 Non-conductive portion:</p>  <table border="1" data-bbox="485 1205 1171 1361"> <tr> <td>y: Chip width</td> <td>x: Chip length</td> <td>z: Chip thickness</td> </tr> <tr> <td><math>y \leq L</math></td> <td><math>x \leq 1/8a</math></td> <td><math>0 &lt; z \leq t</math></td> </tr> </table> <p>⊙ If there chipped area touches the ITO terminal, over 2/3 of the ITO must remain and be inspected according to electrode terminal specifications.            ⊙ If the product will be heat sealed by the customer, the alignment mark must not be damaged.</p> <p>8.1.3 Substrate protuberance and internal crack</p>  <table border="1" data-bbox="804 1675 1246 1832"> <tr> <td>y: width</td> <td>x: length</td> </tr> <tr> <td><math>y \leq 1/3L</math></td> <td><math>X \leq a</math></td> </tr> </table>	y: Chip width	x: Chip length	z: Chip thickness	$y \leq 0.5\text{mm}$	$x \leq 1/8a$	$0 < z \leq t$	y: Chip width	x: Chip length	z: Chip thickness	$y \leq L$	$x \leq 1/8a$	$0 < z \leq t$	y: width	x: length	$y \leq 1/3L$	$X \leq a$	2.5
y: Chip width	x: Chip length	z: Chip thickness																	
$y \leq 0.5\text{mm}$	$x \leq 1/8a$	$0 < z \leq t$																	
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y: width	x: length																		
$y \leq 1/3L$	$X \leq a$																		

NO	Item	Criterion	AQL
09	Cracked glass	The LCD with extensive crack is not acceptable.	2.5
10	Backlight elements	10.1 Illumination source flickers when lit. 10.2 Spots or scratches that appear when lit must be judged. Using LCD spot, lines and contamination standards. 10.3 Backlight doesn't light or color is wrong.	2.5 2.5 0.65
11	Bezel	Bezel must comply with product specifications.	2.5
12	PCB、COB	12.1 COB seal may not have pinholes larger than 0.2mm or contamination. 12.2 COB seal surface may not have pinholes through to the IC. 12.3 The height of the COB should not exceed the height indicated in the assembly diagram. 12.4 There may not be more than 2mm of sealant outside the seal area on PCB. And there should be no more than three places. 12.5 Parts on PCB must be the same as on the production characteristic chart, There should be no wrong parts, missing parts or excess parts. 12.6 The jumper on the PCB should conform to the product characteristic chart.	2.5 2.5 2.5 2.5 0.65 0.65
13	FPC	13.1 FPC terminal damage $\leq$ 1/2 FPC terminal width and can not affect the function , we judge accept. 13.2 FPC alignment hole damage $\leq$ 1/2 alignment area and can not affect the function , we judge accept.	2.5 2.5
14	Soldering	14.1 No cold solder joints, missing solder connections, oxidation or icicle. 14.2 No short circuits in components on PCB or FPC.	2.5 0.65

NO	Item	Criterion	AQL												
15	Touch Panel Chipped glass	<p>Symbols:            x: Chip length                      y: Chip width                      z: Chip thickness            k: Seal width                      t: Touch Panel Total thickness    a: LCD side length            L: Electrode pad length</p> <p>15.1 General glass chip:            15.1.1 Chip on panel surface and crack between panels:</p>  <table border="1" data-bbox="343 801 1161 1019"> <tr> <td>z: Chip thickness</td> <td>y: Chip width</td> <td>x: Chip length</td> </tr> <tr> <td><math>Z \leq t</math></td> <td><math>\leq 1/2 k</math> and not over viewing area</td> <td><math>x \leq 1/8a</math></td> </tr> </table> <p>⊙ Unit: mm            ⊙ If there are 2 or more chips, x is the total length of each chip</p> <p>15.1.2 Corner crack:</p>  <table border="1" data-bbox="343 1400 1161 1617"> <tr> <td>z: Chip thickness</td> <td>y: Chip width</td> <td>x: Chip length</td> </tr> <tr> <td><math>z \leq t</math></td> <td><math>\leq 1/2 k</math> and not over viewing area</td> <td><math>x \leq 1/8a</math></td> </tr> </table> <p>⊙ Unit: mm            ⊙ If there are 2 or more chips, x is the total length of each chip</p>	z: Chip thickness	y: Chip width	x: Chip length	$Z \leq t$	$\leq 1/2 k$ and not over viewing area	$x \leq 1/8a$	z: Chip thickness	y: Chip width	x: Chip length	$z \leq t$	$\leq 1/2 k$ and not over viewing area	$x \leq 1/8a$	2.5
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NO	Item	Criterion	AQL										
16	Touch Panel(Fish eye、dent and bubble on film)	<table border="1"> <thead> <tr> <th>SIZE(mm)</th> <th>Acceptable Q'ty</th> </tr> </thead> <tbody> <tr> <td><math>\Phi \leq 0.2</math></td> <td>Accept no dense</td> </tr> <tr> <td><math>0.2 &lt; D \leq 0.4</math></td> <td>5</td> </tr> <tr> <td><math>0.4 &lt; D \leq 0.5</math></td> <td>2</td> </tr> <tr> <td><math>0.5 &lt; D</math></td> <td>0</td> </tr> </tbody> </table>  	SIZE(mm)	Acceptable Q'ty	$\Phi \leq 0.2$	Accept no dense	$0.2 < D \leq 0.4$	5	$0.4 < D \leq 0.5$	2	$0.5 < D$	0	2.5
SIZE(mm)	Acceptable Q'ty												
$\Phi \leq 0.2$	Accept no dense												
$0.2 < D \leq 0.4$	5												
$0.4 < D \leq 0.5$	2												
$0.5 < D$	0												
17	Touch Panel Newton ring	Newton ring dimension $\leq 1/2$ touch panel area and not affect font and line distortion( $\leq 2.5\%$ ) , it is acceptable.	2.5										
18	Touch Panel Linearity	Less than 2.5% is acceptable.	2.5										
19	LCD Ripple	Touch the touch panel , can not see the LCD ripple. Pen: R 1.0mm silicon rubber. Operation Force: 80g	2.5										
20	General appearance	20.1 Pin type must match type in specification sheet. 20.2 LCD pin loose or missing pins. 20.3 Product packaging must the same as specified on packaging specification sheet. 20.4 Product dimension and structure must conform to product specification sheet.	0.65 0.65 0.65 0.65										

## **12. Handling Precaution:**

### 12-1 Handling of LCM

- Don't give external shock.
- Don't apply excessive force on the surface.
- Liquid in LCD is hazardous substance. Must not lick and swallow. when the liquid is attach to your hand, skin, cloth etc. Wash it out thoroughly and immediately.
- Don't operate it above the absolute maximum rating.
- Don't disassemble the LCM.
- The operators should be grounded whenever he/she comes into contact with the module. Never touch any of the conductive parts such as the LSI pads, the copper leads on the PCB and the interface terminals with any parts of the human body.
- The modules should be kept in antistatic bags or other containers resistant to static for storage.
- The module is coated with a film to protect the display surface. Be care when peeling off this protective film since static electricity may be generated.

### 12-2 Storage

- Store in an ambient temperature of  $25\pm 10^{\circ}\text{C}$ , and in a relative humidity of  $50\pm 10\%\text{RH}$ . Don't expose to sunlight or fluorescent light.
- Storage in a clean environment, free from dust, active gas, and solvent.
- Store in anti-static electricity container.
- Store without any physical load.

### 12-3 Soldering

- Use only soldering irons with proper grounding and no leakage.
- Iron: No higher than  $310\pm 10^{\circ}\text{C}$  and less than 3 sec during Hand soldering.
- Rewiring: no more than 2 times.

## **13. Warranty**

This product has been manufactured to specifications as a part for use in your company's general electronic products. It is guaranteed to perform according to delivery specifications. For any other use apart from general electronic equipment, we will not take responsibility if the product is used in medical devices, nuclear power control equipment, aerospace equipment, fire and security systems, or any other applications in which there is a direct risk to human life and where extremely high levels of reliability are required. If the product is to be used in any of the above applications, we will need to enter into a separate product liability agreement.

1. We cannot accept responsibility for any defect arise after additional process of the product (including disassembly and reassembly), after product delivery.
2. We cannot accept responsibility for any defect, which may arise after the application of strong external force to the product.
3. We cannot accept responsibility for any defect, which may arise due to the application of static electricity after the product has passed your company's acceptance inspection procedures.
4. We can not accept responsibility for industrial property, which may arise through the use of your product, with exception to those issues relating directly to the structure or method of manufacturing of our product within one year from YEEBO shipment.
5. For Heatseal Product which required to heatseal by customer side, parts must be used within three months after delivery from factory.
6. For TAB Product which required to solder by customer side, parts must be used within three months after delivery from factory.
7. The liability of YB is limited to repair or replacement on the terms set forth below. YB will not be responsible for any subsequent or consequential events or injury or damage to any personnel or

user including third party personnel and/or user. Unless otherwise agreed in writing between YB and the customer, YB will only replace or repair any of its LCD which is found defective electrically or visually when inspected in accordance with YB GENERAL LCD INSPECTION STANDARD.

#### **14. Guarantee:**

Our products meet requirements of the environment.  
YEEBO ROHS requirement is based on European Union Directive 2011/65/EU (ROHS) Requirements and Update.