



PLC Stamp mini 2 Datasheet

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1 Revisions

Revision	Release Date	Changes
1	November 16, 2015	initial issue

2 Abstract

The PLC (PowerLine Communication) module gives your application access to powerline communication based on the HomePlug® Green PHY™ Chip QCA7000 / QCA7005. You can realize point-to-point and multi-point connections depending on your application. The data will be transmitted as Ethernet packets over the power line. This gives you the opportunity to use TCP/IP or whatever network protocols you wish to use.

This module includes the needed isolation between the power line and the secondary signals. You can freely design the power supply external to the module so that it perfectly meets the requirements for your application.

The QCA7000 / QCA7005 by Qualcomm Atheros ensures compatibility with many other commercial powerline devices.

Parameter	Value
Power supply	3.3 V
Power consumption	0.5 W
Data rate	max. 10 MBit/s
Reach	maximal 300 m over the Powerline
Temperature range	-40 °C - 85 °C (industrial) / 0 °C - 70 °C (commercial)
Outline dimension	43.5 mm x 22 mm x 6.5 mm
Weight	5.6 g
RoHS	PLC Stamp mini 2 is manufactured in compliance with RoHS

3 Applications

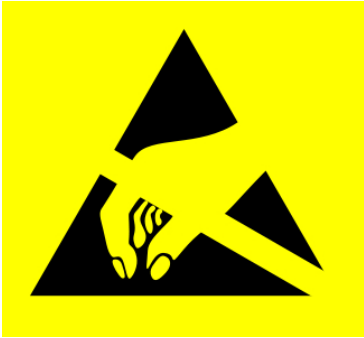
- interconnection of household appliances to the Smart Grid
- connection of smart meters to Smart Meter Gateways and/or LAN/WAN/WiFi
- connection of sensors
- connection of photovoltaic equipment
- connection of heating and air conditioning system
- coupling of machines and measurement devices
- forwarding of digital signals (remote I/O)
- coupling of RF cells for home automation

4 Interfaces

Powerline: 230 V AC, 110 V AC, DC, dead-wire 2-wire-connections

Serial interfaces: UART or SPI (order option)

5 Handling



This electronic component is sensitive to electrostatic discharge (ESD).
The module contains components with **moisture sensitivity level (MSL) 3**. Please handle them accordingly.

6 Module Overview

The block diagram in Figure 1 shows module components in the gray box as well as the connections and external components that you need in addition.

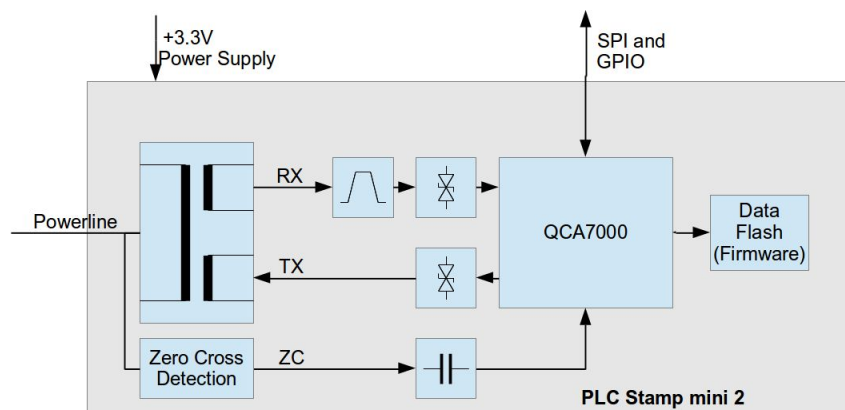


Figure 1: Block Diagram of PLC Stamp mini 2

Parts of the module that are not related to isolation are located below a metal shield. Information about the module are printed on a high-temperature label on top of this shield.

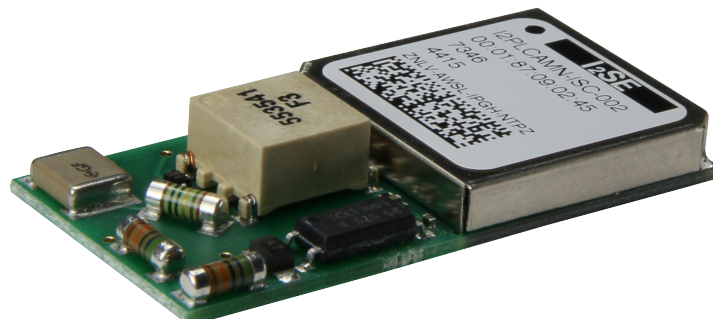


Figure 2: Image of the Module

7 Technical Data

7.1 Absolute Maximum Ratings

SYMBOL	PARAMETER	Min.	Max.	UNIT
VDD	Digital supply voltage	-0.3	3.46	V
VDIO	Digital input voltage	-0.3	3.63	V
TSTORE	Storage temperature	-40	150	°C
RAH	Relative air humidity (not condensing)	10	90	%

7.2 Operating Conditions

SYMBOL	PARAMETER	Min.	Typ.	Max.	UNIT
VDD	Digital supply voltage	3.13	3.3	3.46	V
IDD	Current for VDD	-	150 (average)	300	mA
VAC	Mains Voltage (AC)	85	110 / 230	250	V
VDC (1)	Mains Voltage (DC)	-	-	380	V
fAC (2)	Mains Frequency Tolerance (50Hz)	48.5	50	51.5	Hz
	Mains Frequency Tolerance (60Hz)	57.9	60	62.1	Hz
TCASE	Top of case temperature (industrial)	-40	-	85	°C
	Top of case temperature (iomedical)	0	-	70	°C

Notes:

1. DC Voltage is specified as the sum of all AC and DC parts of the line.
2. If you couple PLC Stamp Mini 2 to a DC Line make sure to tie the ZC.IN pin to low via a 10 kOhm resistor.

8 Firmware and MAC Addresses

These modules are pre-programmed with firmware and parameter information block (PIB). The PIB contains the MAC addresses of the module, prescaler values defining the output power as well as settings for automotive use of the QCA7000 / QCA7005 (SLAC). The MAC address uses a prefix (organizationally unique identifier) that is assigned to I2SE. The prescaler values that are set in the production process were defined by Qualcomm for the intended application / market (see order option Parameter Optimization). Automotive variants include settings to enable SLAC for either the EVSE or the PEV side.

9 Module Pinout

PIN	Direction	Name	Description
1	IN/OUT	GPIO_0	QCA7000 / QCA7005 GPIO 0
2	IN/OUT	GPIO_1	QCA7000 / QCA7005 GPIO 1
3	IN/OUT	GPIO_2	QCA7000 / QCA7005 GPIO 2
4	IN/OUT	GPIO_3	QCA7000 / QCA7005 GPIO 3
5	IN	RESET_L	Reset (low active)
6	IN/OUT	SERIAL_4	Serial_4 of QCA7000 / QCA7005
7	IN/OUT	SERIAL_3	Serial_3 of QCA7000 / QCA7005
8	IN/OUT	SERIAL_2	Serial_2 of QCA7000 / QCA7005
9	IN/OUT	SERIAL_1	Serial_1 of QCA7000 / QCA7005
10	IN/OUT	SERIAL_0	Serial_0 of QCA7000 / QCA7005
11	SUPPLY	GND	Ground connection
12	SUPPLY	VDD	Supply Voltage for the module
13	IN	N.C. / ZC_IN	do not connect (home automation variant), zero cross detection input (automotive variant)
14	-	-	not available
15	IN/OUT	N	Powerline communication
16	IN/OUT	L	Powerline communication
17	IN/OUT	L	Powerline communication
18*	SUPPLY	GND	Ground connection
19*	SUPPLY	GND	Ground connection
20*	SUPPLY	GND	Ground connection
21*	SUPPLY	GND	Ground connection
22*	SUPPLY	GND	Ground connection
23*	SUPPLY	GND	Ground connection
24*	SUPPLY	GND	Ground connection
25*	SUPPLY	GND	Ground connection

Table 6: Pinout of the PLC Stamp Mini 2 Module

* not needed for electrical function, only for SMD assembly

9.1 GPIO

9.1.1 Power-on Configuration

The QCA7000 / QCA7005 comprises four GPIO pins which are read at boot time to get the desired configuration. Table 8 shows the configuration the QCA7000 / QCA7005 at boot time.

GPIO #	Function	Pull Up	Pull Down	Preload on Module
0	Boot Source	Flash	Host	-
1	Host Interface	-	SPI Slave	10 kOhm Pull Down
2	SPI Slave Mode	Burst	Legacy	-
3	None	-	-	-

Table 8: QCA7000 / QCA7005 Boot Strap Options

9.1.2 General Purpose I/O Functions

The GPIOs of the QCA7000 / QCA7005 have different functions after booting. They can either be used as input or output to display various states or trigger some actions. It is not possible to use these pins from your own application - only the QCA7000 / QCA7005 firmware can control these GPIOs.

The GPIOs are set up as noted in Table 10.

GPIO #	Direction	Function
0	Output	Pushbutton Simple Connect (blinking with 1Hz: simple connect mode active, off: not in simple connect mode)
1	Output	unused in default configuration
2	Output	PLC Connection (high=established connection, low=no connection)
3	Input (lowactive)	Start Pushbutton Simple Connect

Table 10: QCA7000 / QCA7005 GPIO Settings

9.2 Serial Signals

The signals SERIAL_0 to SERIAL_4 represent the SPI or UART signals. Table 12 contains the UART and SPI function for each signal.

Signal Name	SPI function	UART function
SERIAL_0	Interrupt	
SERIAL_1	CLK	RTS
SERIAL_2	CS	CTS
SERIAL_3	MISO	TXD
SERIAL_4	MOSI	RXD

Table 12: QCA7000 / QCA7005 UART/SPI Signals

Please note that the selection between SPI and UART mode is effected using different QCA7000 / QCA7005 firmware and is therefore an order option.

9.2.1 UART

The UART supports baud rates of up to 115,200 baud comprises four signal pins: TX, RX, RTS and CTS.

9.2.2 SPI

The QCA7000 / QCA7005 uses SPI in mode 3: CPOL=1, CPHA=1.

SPI should be used in burst mode, meaning that the Chip Select signal is kept low during a complete SPI message.

The SPI CLK period should not be less than 83.3 ns resulting in a maximum clock frequency of 12 MHz.

10 Recommended Footprint

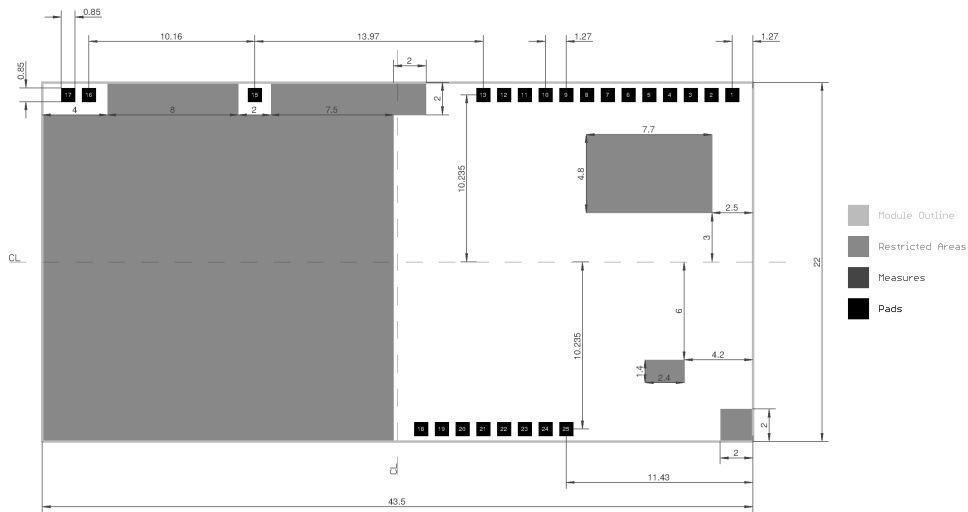


Figure 3: Recommended Footprint

Notes:

1. All dimensions are in mm.
2. Pads are all of the same size.
3. Distances between pads are equal if not otherwise specified in the drawing.
4. Pad 14 is not available for compatibility to the previous footprint of PLC Stamp mini
5. The drawing in Figure 3 shows the top view on the footprint (as if you look through the module).
6. Restricted areas should be kept free of copper on the base PCB.
7. The module outline shows the ideal measures - tolerance is not included.

11 Getting Started

The module does not need other parts for PLC connection. You need to connect it to:

1. 3.3 V supply
2. your processor (via SERIAL_0 through SERIAL_4)
3. to L and N of your mains circuit or to GND and CP for automotive uses (see order variants)

Refer to Table 6 to see how the module needs to be connected.

The following two sections detail the difference between mains connected and automotive applications.

11.1 CE Class B / North American Variant

The PLC Transformer used for these variants has a turns ratio of 1:4:5 and the Zero Cross Detection circuit is present, which is necessary for Powerline communication over the mains. Let the zero cross detection (Pin 13) floating.

The GPIO pins, the reset pin and the ZC pin do not need to be connected.

11.2 Automotive EVSE / PEV

The PLC Transformer has a turns ratio of 1:1:1.

These variants are not equipped with a Zero Cross Detection circuit. Some devices shall be capable to detect the zero cross of the AC line to support coexistence functionality. In this case refer to Figure 4. Connect signals L and N of Figure 4 to mains neutral and line and ZC_IN to pin 13 of the module. Usually the EVSE should have the zero cross detection while PEV naturally do not need it.

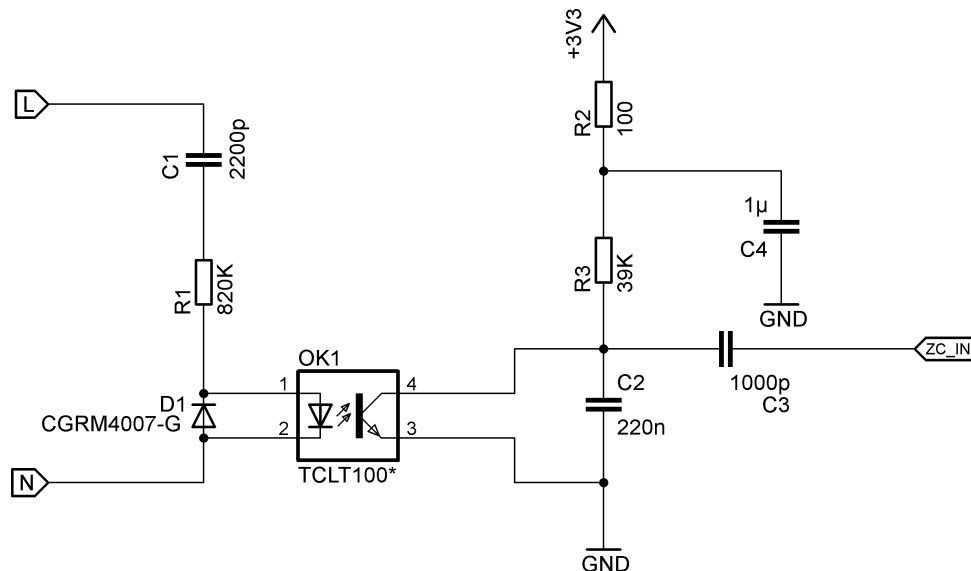


Figure 4: Schematic Zerocross Detection

Automotive variants of PLC Stamp mini 2 are not designed to work on mains. Connect pin 16 and 17 of the module to GND and use pin 15 for the powerline communication. Do not connect pin 15-17 to the AC line. The GPIO pins, and the reset pin do not need to be connected.

12 Module Marking

Each Module is marked with a label containing the following data:

1. Order Code
2. QCA7000 / QCA7005 MAC Address (printed with a separating colon every two digits, the DataMatrix code contains the digits without separator)
3. Serial Number
4. Production Date Code: WWYY
5. Device Security Key
6. 2D DataMatrix code containing the above information as a list of space separated values

Additionally each label contains:

1. I2SE Banner
2. Pin 1 Marking

An example is shown in Figure 5.



Figure 5: Example Label for PLC Stamp Mini 2

13 Order Information

The following table provides an overview of the available PLC Stamp mini 2 variants.

Order code	Chip	Temperature Range / °C	Serial Interface	Application	PLC Transformer	Zero Cross Detection	Availability
I2PLCAMN-ISC-002	QCA7000	-40 - 85	SPI	CE Class B	I2PLCTR-1	on module	standard
I2PLCAMN-ISN-002	QCA7000	-40 - 85	SPI	North America	I2PLCTR-1	on module	on request
I2PLCAMN-ISE-002	QCA7000	-40 - 85	SPI	Automotive EVSE	I2PLCTR-2	external	on request
I2PLCAMN-ISP-002	QCA7000	-40 - 85	SPI	Automotive PEV	I2PLCTR-2	external	on request
I2PLCAMN-IUC-002	QCA7000	-40 - 85	UART	CE Class B	I2PLCTR-1	on module	on request
I2PLCAMN-IUN-002	QCA7000	-40 - 85	UART	North America	I2PLCTR-1	on module	on request
I2PLCAMN-IUE-002	QCA7000	-40 - 85	UART	Automotive EVSE	I2PLCTR-2	external	on request
I2PLCAMN-IUP-002	QCA7000	-40 - 85	UART	Automotive PEV	I2PLCTR-2	external	on request
I2PLCAMN-CSC-002	QCA7000	0 - 70	SPI	CE Class B	I2PLCTR-1	on module	on request
I2PLCAMN-CSN-002	QCA7000	0 - 70	SPI	North America	I2PLCTR-1	on module	on request
I2PLCAMN-CSE-002	QCA7000	0 - 70	SPI	Automotive EVSE	I2PLCTR-2	external	on request
I2PLCAMN-CSP-002	QCA7000	0 - 70	SPI	Automotive PEV	I2PLCTR-2	external	on request
I2PLCAMN-CUC-002	QCA7000	0 - 70	UART	CE Class B	I2PLCTR-1	on module	on request
I2PLCAMN-CUN-002	QCA7000	0 - 70	UART	North America	I2PLCTR-1	on module	on request
I2PLCAMN-CUE-002	QCA7000	0 - 70	UART	Automotive EVSE	I2PLCTR-2	external	on request
I2PLCAMN-CUP-002	QCA7000	0 - 70	UART	Automotive PEV	I2PLCTR-2	external	on request
I2PLCBMN-ISC-002	QCA7005	-40 - 85	SPI	CE Class B	I2PLCTR-1	on module	on request
I2PLCBMN-ISN-002	QCA7005	-40 - 85	SPI	North America	I2PLCTR-1	on module	on request
I2PLCBMN-ISE-002	QCA7005	-40 - 85	SPI	Automotive EVSE	I2PLCTR-2	external	on request
I2PLCBMN-ISP-002	QCA7005	-40 - 85	SPI	Automotive PEV	I2PLCTR-2	external	on request
I2PLCBMN-IUC-002	QCA7005	-40 - 85	UART	CE Class B	I2PLCTR-1	on module	on request
I2PLCBMN-IUN-002	QCA7005	-40 - 85	UART	North America	I2PLCTR-1	on module	on request
I2PLCBMN-IUE-002	QCA7005	-40 - 85	UART	Automotive EVSE	I2PLCTR-2	external	on request
I2PLCBMN-IUP-002	QCA7005	-40 - 85	UART	Automotive PEV	I2PLCTR-2	external	on request

Table 14: PLC Stamp mini 2 Order Codes

Product Family Code	Chip		Temperature Range	Serial Interface	Parameter Optimization	Opti-	Version
I2PLC	A: QCA7000	MN-	I: Industrial (-40 - 85 °C)	S: SPI	C: CE Class B		-002
	B: QCA7005		C: Commercial (0 - 70 °C) - only for QCA7000	U: UART	N: North America		
					E: Automotive EVSE		
					P: Automotive PEV		

Table 16: PLC Stamp mini 2 Order Code Compilation

14 Contact

Website: <http://www.i2se.com>

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