

Features

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High Performance Non-PCI Interface

- Configurable 8/16/32-bit SRAM-like host interface, easily interfaces with most common embedded MCUs (AX88782: 8/16-bit, AX88783: 8/16/32-bit)
- Supports PPPoE/IPv4 IP/TCP/UDP/ICMP/ IGMP checksum offload to relieve CPU loading
- Supports burst-mode access, minimizing CPU overhead

Fast Ethernet MAC/PHY

- IEEE 802.3 10Base-T/100Base-TX compatible
- Supports full-duplex operation with IEEE 802.3x flow control and half duplex with backpressure
- 10/100M PHY supports twisted pair crossover detection and auto-correction (HP Auto-MDIX)
- Supports Wake-on-LAN by Microsoft Wakeup Frame, Magic Packet and link status change detection

Switching Fabric

- Performs non-blocking wire-speed forwarding and filtering
- Embeds 32KB SRAM for packet buffering
- Supports broadcast storm filtering
- Supports per queue and per port ingress and egress programmable rate limit control (4K ~ 100M bps)
- Integrated two-way Address-Lookup engine and table for 1K MAC addresses
- Supports Routing Table/IGMP/VLAN Table access through CPU read/write operation
- Supports 802.1D Spanning Tree Protocol and 802.1w Rapid Spanning Tree Protocol.

QoS

- Supports Quality-of-Service for Port-Based, 802.1p VLAN and IPv4 TOS/IPv6 COS packets with four priority queues
- Supports RFC2475 DiffServ-based

VLAN

- Supports up to 3 VLAN groups for port-based VLAN and 16 VLAN entries for 802.1Q Tag-based VLAN function
- Supports Double tagging 802.1Q-in-802.1Q

Product Brief

function for WAN access

Security

- Supports ingress port security mode, incoming packets with unknown source MAC address could be dropped
- Supports eight Security MAC Registrations
- Supports 802.1X Port-Based Authorization

Multicast

- Supports GMRP/GVRP/GARP/LACP packet snooping
- Supports up to 1K Multicast Group (shared with L2 MAC table)
- Support eight IGMP Multicast IP address snooping

Monitoring

- Supports RMON group 1,2,3,9 counter (RFC1213)
- Supports Ethernet-like MIB counter (RFC 1643)
- Supports Bridge MIB counter (RFC 1493)
- Egress/Ingress Port Mirroring
- Sniffer functions:
 - Source/Destination Port
 - DA/SA
 - VLAN ID
- Ethernet Packet Type
- IPv4/IPv6 Protocol
- ◆ IPv4/IPv6 TCP/UDP Port Number

Optional Interfaces Supported:

- MII or Reverse-MII
- RMII or Reverse-RMII
- Optional serial EEPROM
- Integrates an on-chip voltage regulator requiring only a single power supply of 3.3V
- Integrates an on-chip oscillator and PLL requiring only a 25Mhz crystal to operate
- Integrates on-chip power-on reset circuit
- Small form factor: 80-pin LQFP (AX88782) or 128-pin LQFP (AX88783) RoHS compliant package
- Operating temperature range: 0°C to 70°C

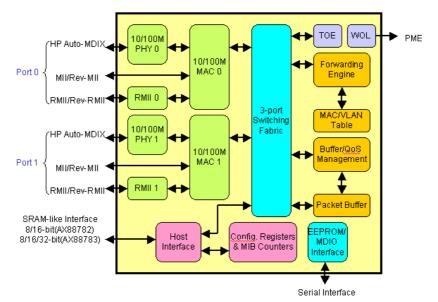
Product Description

The AX88782/AX88783 is a non-PCI 2-port 10/100M Ethernet controller which integrates a 3-port switching fabric, three 10/100M MACs, two 10/100M PHYs, and an 8/16/32-bit SRAM-like host interface. This controller is targeted at embedded system applications that need to support two Ethernet ports, typically one for a LAN port and one for a WAN port. The AX88782/AX88783 supports simple a SRAM-like host interface, routine packet checksum calculation, and burst-mode access which make it easy to provide high performance 2-port Ethernet connectivity solutions for any embedded MCU. The built-in switching fabric supports non-blocking wire-speed forwarding and provides four priority queues for advanced QoS functions including Port-Based, 802.1p VLAN, IPv4 TOS/IPv6 COS for voice, video, audio and data traffic classification. The AX88782/AX88783 combines the benefits of high integration and flexibility which makes it an ideal single-chip solution for designing high performance, QoS-aware, cost effective and small form factor 2-port Ethernet function for any embedded system application.

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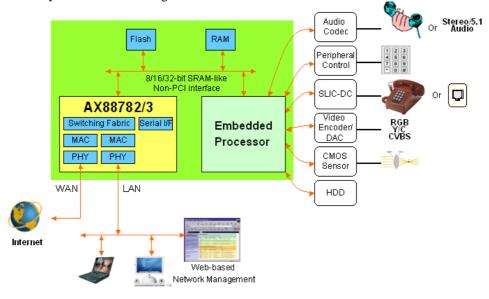


Block Diagram

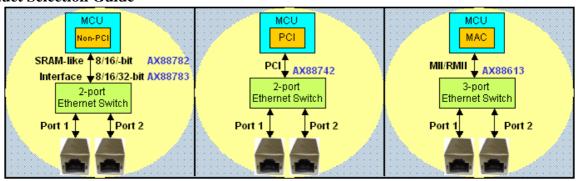


Target Applications

- VoIP Phone, VoIP ATA Adapter
- IP Camera for Remote Surveillance
- Next Generation IP-STB and IPTV
- Industrial Controller and Networked Sensor
- Port Redundancy and Port Monitoring



Product Selection Guide



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