

Kinkan

Module Motherboard

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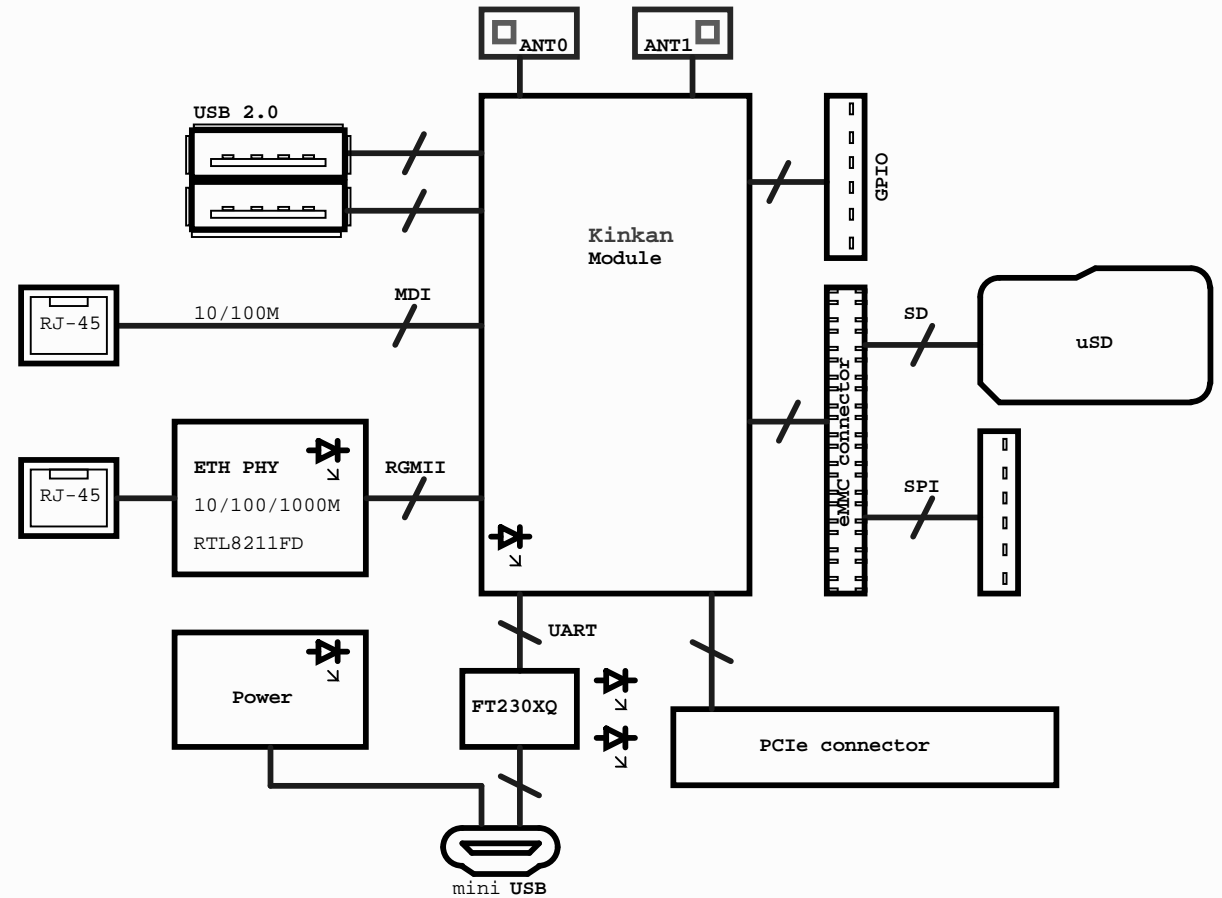
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Revision History

REV	Date	Revision Description
V1.0	2017-09-01	Schematics Released
V2.0	2018-02-21	Schematics Released

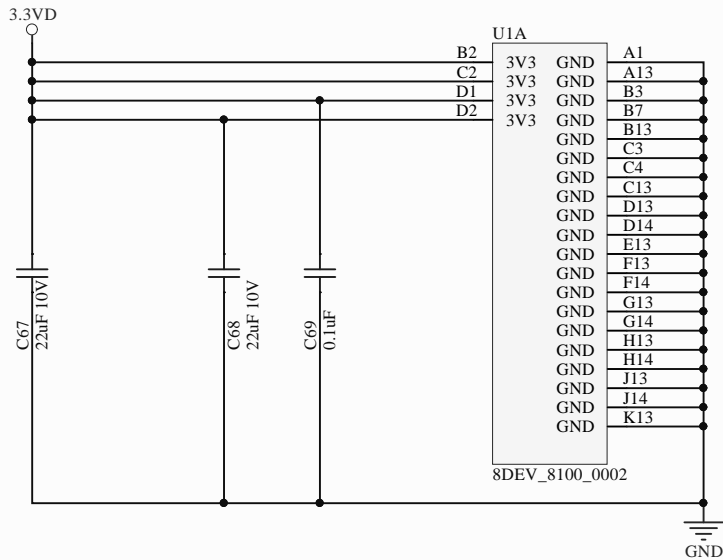
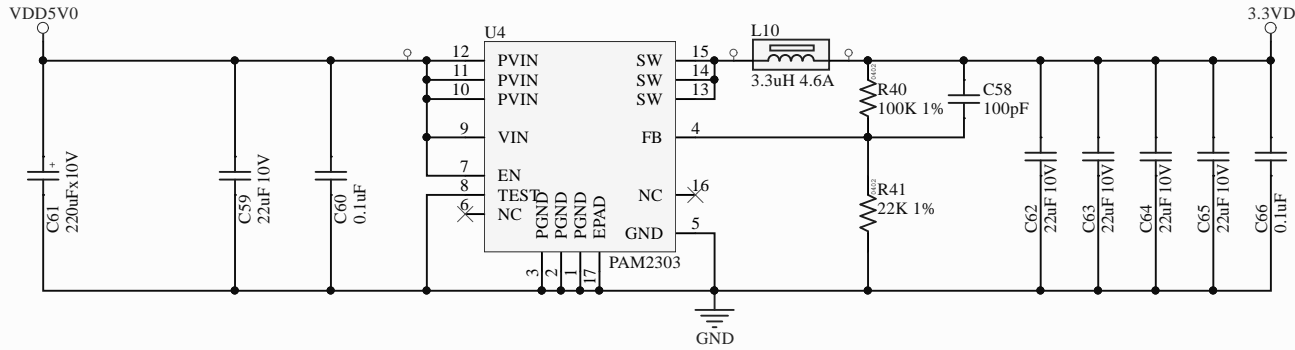
NOTE:


- (1) "NI" stands for the component that is NOT installed.
- (2) "M_" stands for the DDR net prefix.
- (3) "PHY_" stands for the Ethern PHY net prefix.
- (4) "RGMII_" stands for the RGMM interface prefix.



8 devices	
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Title: Title Page	SCH Rev. V2.0
CTO	8 Devices R&D team
Approved By:	Designed By:
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MODULE POWER



	8 devices	
	Model No.: Kinkan	
	Title: Power	SCH Rev. v2.0
	Approved By: CTO	8 Devices R&D team
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ETHERNET PORTS

UIB

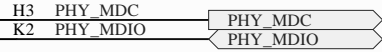
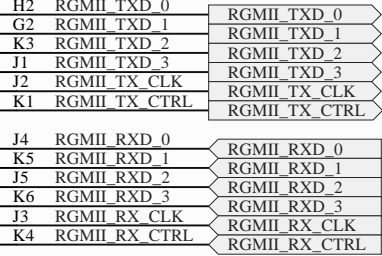
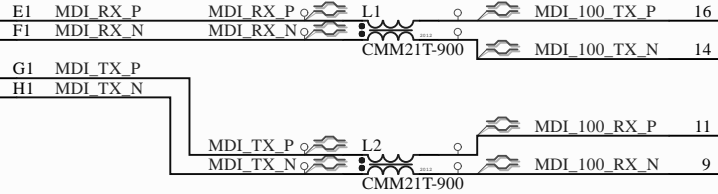
MDI_RXI4_P
MDI_RXI4_N

MDI_TXO4_P
MDI_TXO4_N

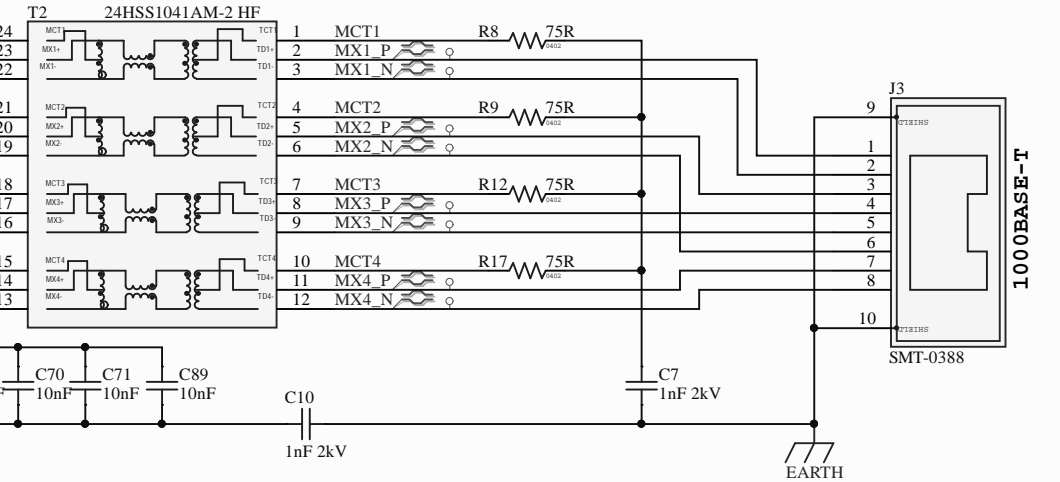
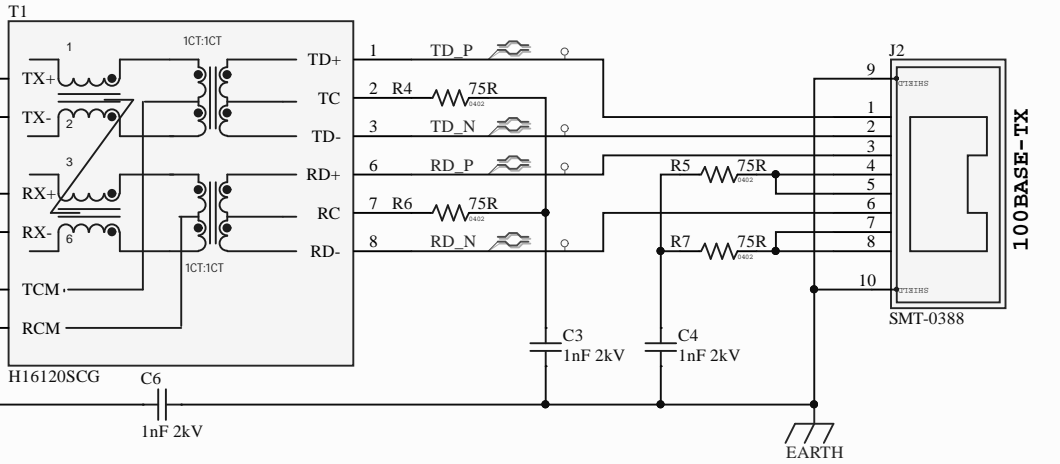
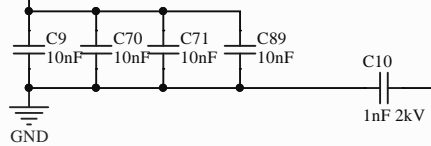
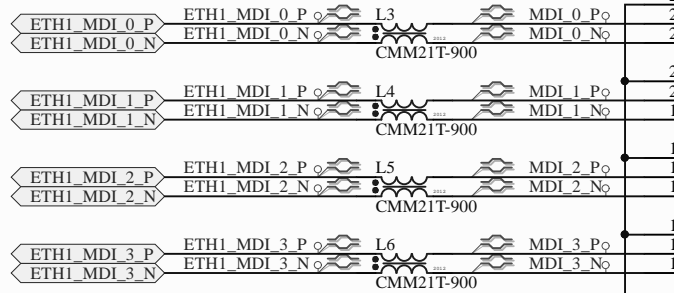
P0_TXD0
P0_TXD1
P0_TXD2
P0_TXD3
P0_TXC
P0_TXCTL


P0_RXD0
P0_RXD1
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P0_RXD3
P0_RXC
P0_RXCTL

P0_MDC
P0_MDIO

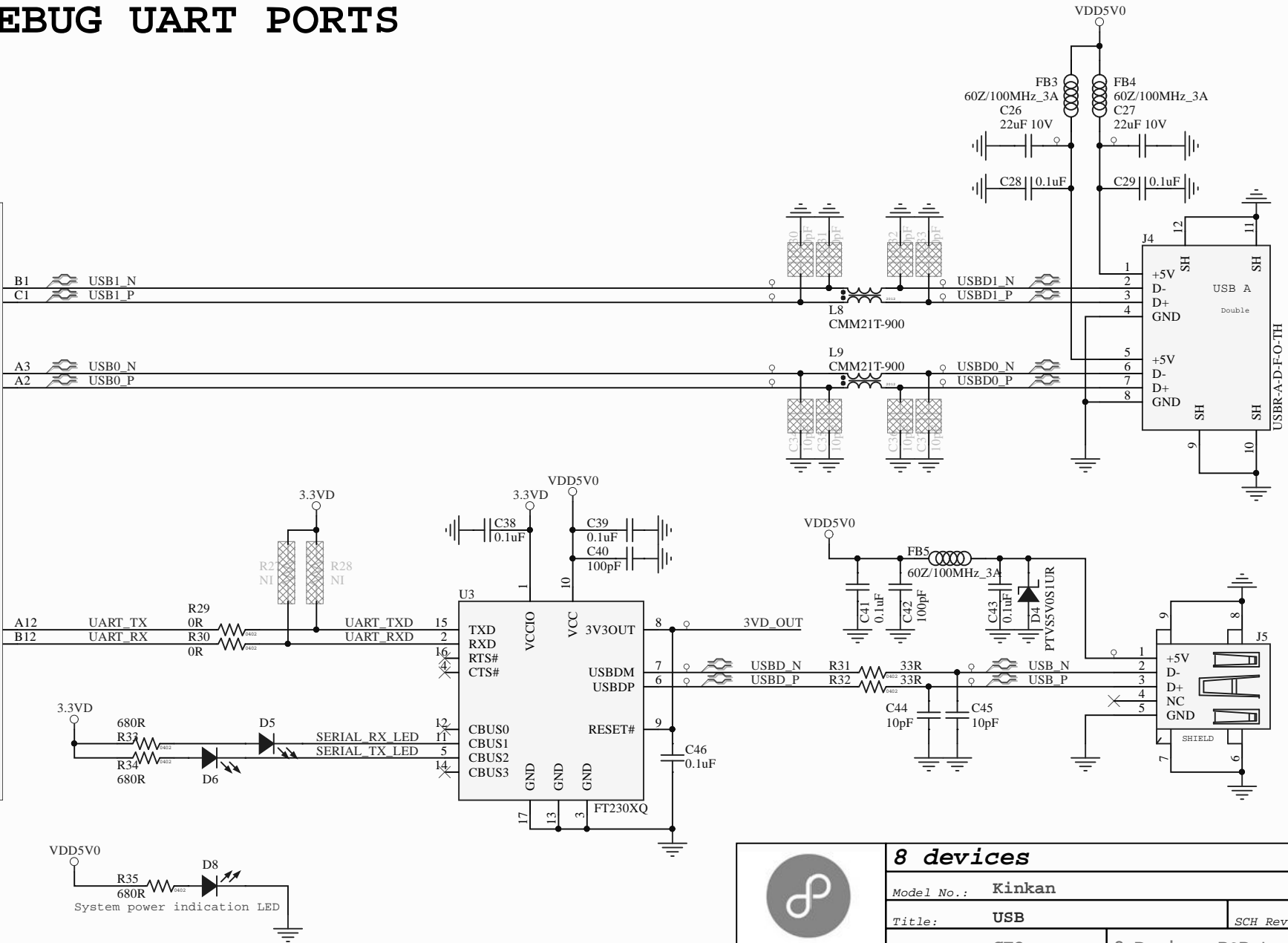
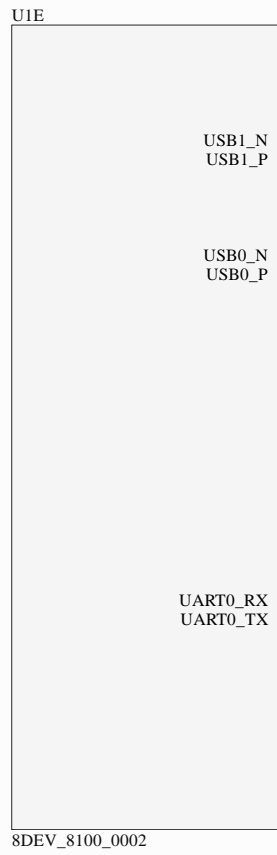



8DEV_8100_0002



	8 devices	
	Model No.: Kinkan	
	Title: ETH	SCH Rev. v2.0
	Approved By: CTO	8 Devices R&D team
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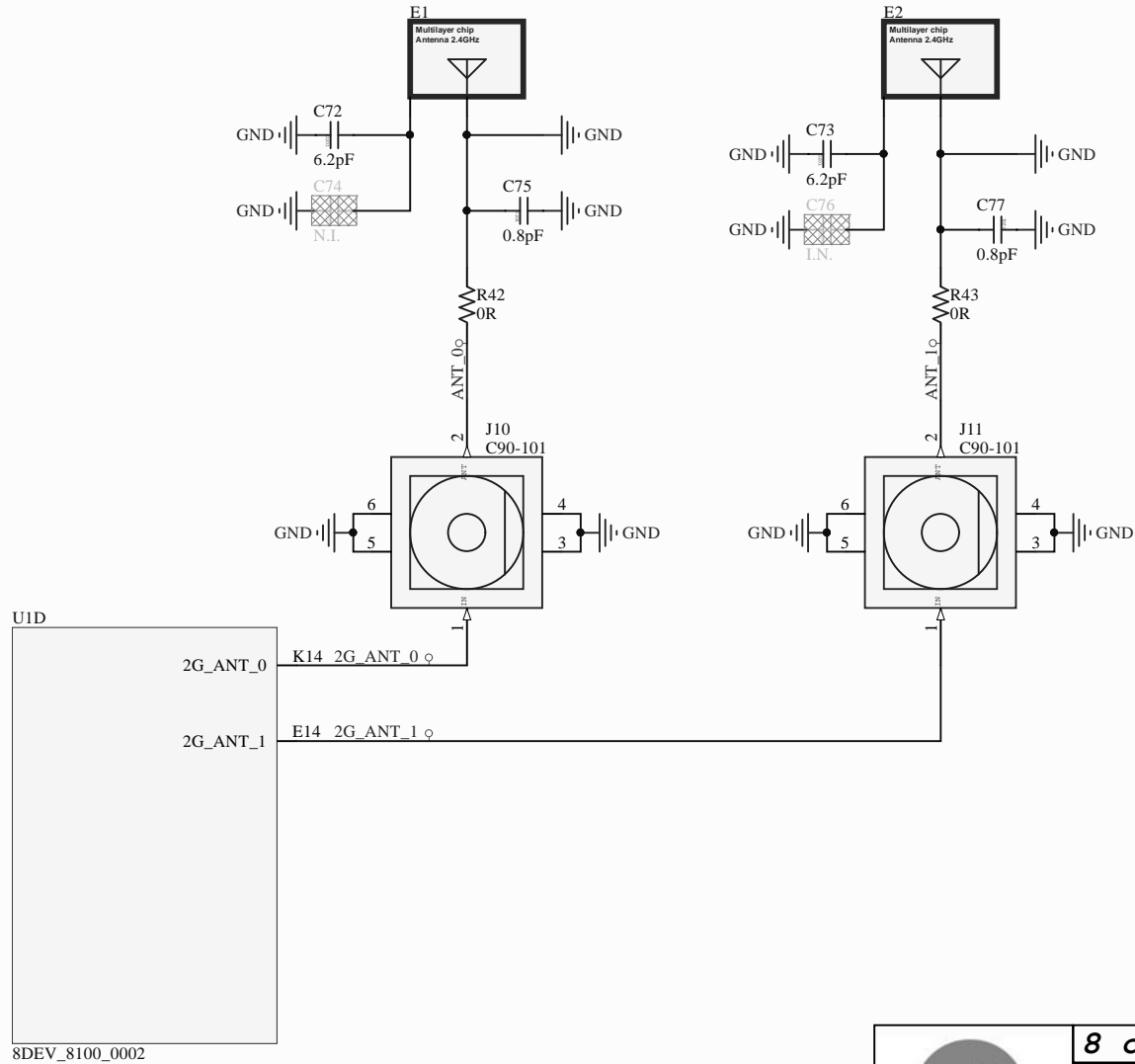
USB & DEBUG UART PORTS




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	Title: USB	
	Approved By: CTO	
	Designed By: 8 Devices R&D team	
	Date: 2018-09-05	

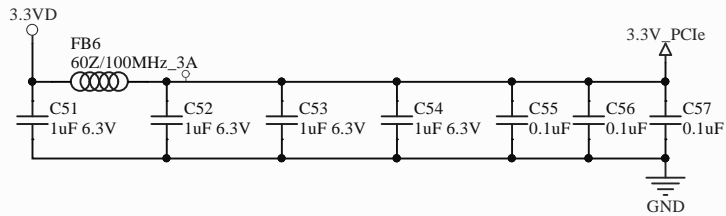
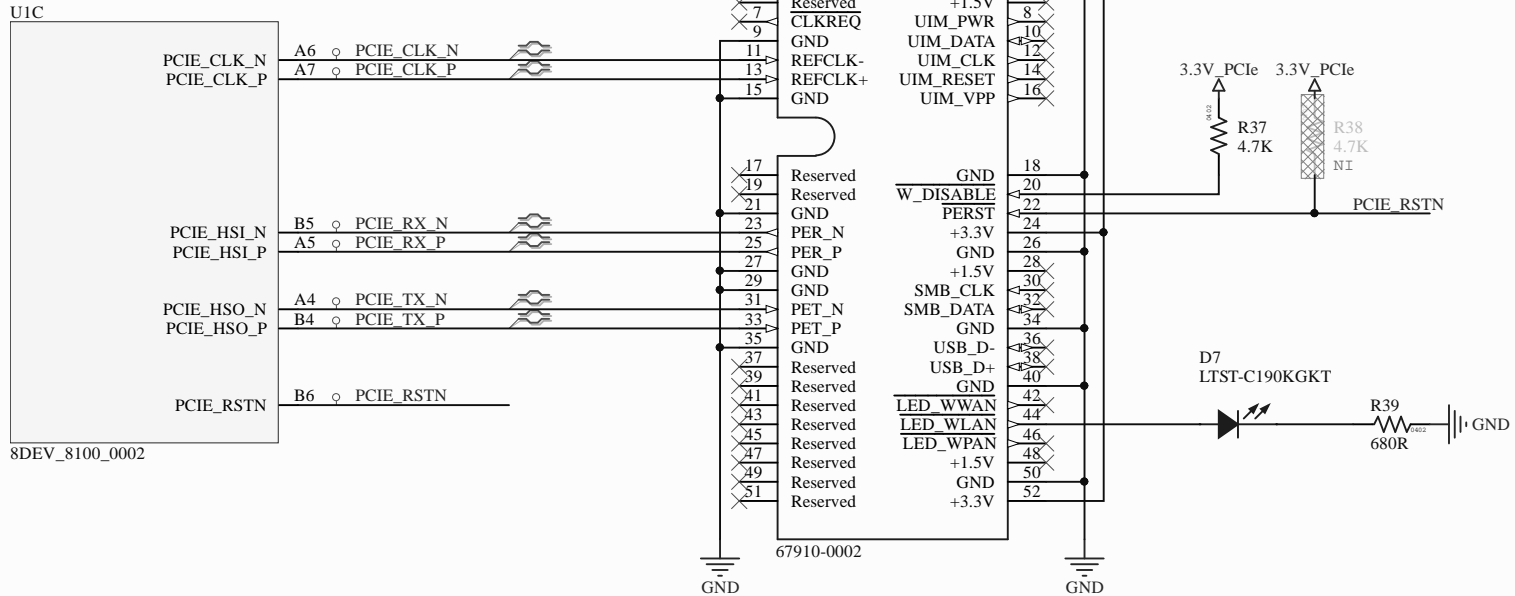
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RF



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PCIe



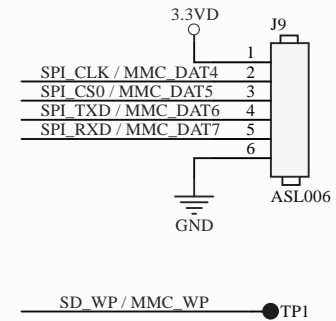
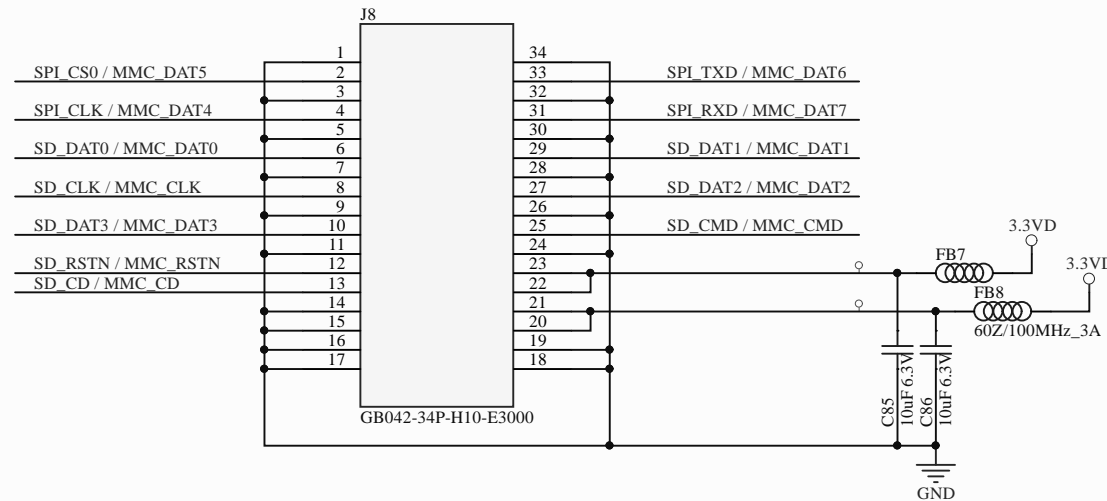
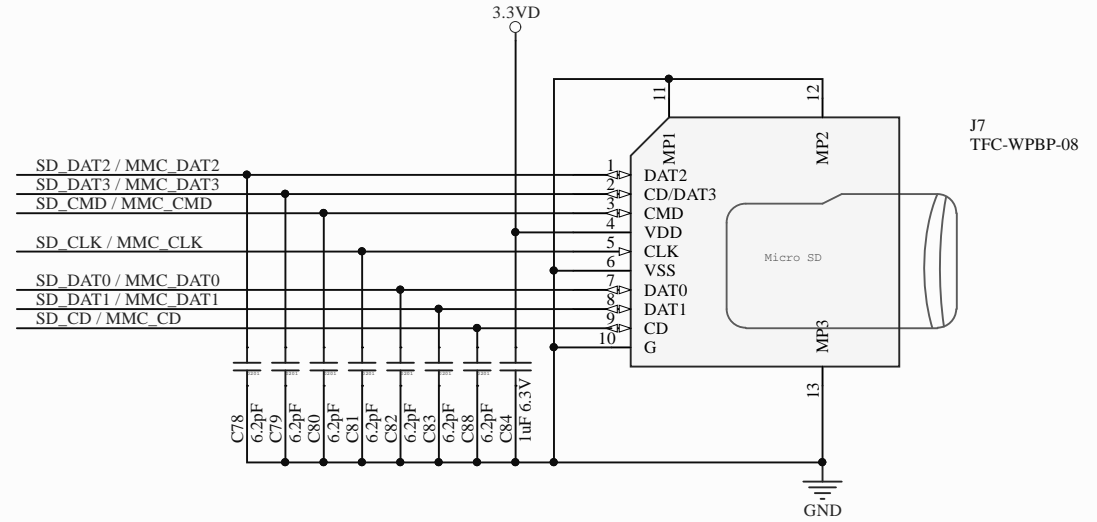
	8 devices	
	Model No.: Kinkan	
	Title: PCIe	SCH Rev. v2.0
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SD / eMMC / SPI

U1H

SD_WP/EMMC_WP/[GPIO_C5]	J6 ◊ SD_WP/MMC_WP
SD_CLK/EMMC_CLK/[GPIO_C6]	J9 ◊ SD_CD/MMC_CD
SD_RSTN/EMMC_RSTN/[GPIO_C4]	K9 ◊ SD_RSTN/MMC_RSTN
SD_CD/EMMC_CD/[GPIO_C7]	K11 ◊ SD_CLK/MMC_CLK
SD_CMD/EMMC_CMD/[GPIO_D0]	J11 ◊ SD_CMD/MMC_CMD
SD_DAT0/EMMC_DAT0/[GPIO_D1]	J10 ◊ SD_DAT0/MMC_DAT0
SD_DAT1/EMMC_DAT1/[GPIO_D2]	K10 ◊ SD_DAT1/MMC_DAT1
SD_DAT2/EMMC_DAT2/[GPIO_D3]	J12 ◊ SD_DAT2/MMC_DAT2
SD_DAT3/EMMC_DAT3/[GPIO_D4]	K12 ◊ SD_DAT3/MMC_DAT3
SPI1_CLK/EMMC_DAT4/[GPIO_D5]	J8 ◊ SPI_CLK/MMC_DAT4
SPI1_CS0/EMMC_DAT5/[GPIO_D6]	K8 ◊ SPI_CS0/MMC_DAT5
SPI1_TXD/EMMC_DAT6/[GPIO_D7]	K7 ◊ SPI_TXD/MMC_DAT6
SPI1_RXD/EMMC_DAT7/[GPIO_E0]	J7 ◊ SPI_RXD/MMC_DAT7

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	8 devices	
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