Mindspeed® Introduces Dual-Core Packet Processors for Carrier-Class Customer Premise Applications

New Family of Comcerto® Devices Supports Sophisticated Applications, High-Throughput and QoS for Next-Generation Broadband Packet Media Services

Newport Beach, Calif., 4. Juni 2007 - Mindspeed Technologies, Inc., (NASDAQ: MSPD), a leading supplier of semiconductor solutions for network infrastructure applications, today announced a new family of dual-core packet processors that will enable service providers to cost-effectively support sophisticated applications, high-performance packet processing and quality of service (QoS) for the triple-play broadband home and small and medium-sized enterprise (SME) markets.

The Comcerto 100 Series of packet processors brings the field proven, carrier-class performance of Mindspeed's integrated system-on-chip (SoC) architecture to the customer premise gateway market. The new family of devices will enable service providers to deploy sophisticated, reliable services with the high-packet throughput required for delivering next-generation multimedia content to their subscribers.

"Our new Comcerto 100 Series provides unprecedented price/performance in a range of pin-compatible devices that will allow customer premise equipment (CPE) manufacturers to design a family of advanced gateway solutions using the same software architecture to support broadband home routers, enterprise service routers, and high-end integrated access devices," said Preet Virk, vice president and business director of Mindspeed's CPE products.

"With CPE equipment based on our Comcerto 100 Series packet processors, service providers will have the application and performance headroom they need to deploy new revenue-generating services to their broadband home and SME subscribers with capabilities such as sophisticated data routing with QoS, carrier-class voice-over-IP (VoIP), IPv6 support, virtual private networks with firewalls, 3DES & null SHA-1 support, packet filtering, as well as 802.11n Wi-Fi and WiMax data rates support for wireless home entertainment networks," Virk added.

Mindspeed designed the Comcerto 100 Series to deliver carrier-class performance at competitive price points by integrating the industry-standard interfaces required for gigabit bandwidth with a secondary cache to optimize operating systems and applications performance, a security processor, dual ARM processors, sophisticated DMA engines, an internal bus architecture and on-chip memory to boost packet throughput. Its flexible, programmable architecture allows either or both ARM cores to be used for applications or packet processing, enabling equipment manufactures to offer multiple solutions tailored for specific deployments, and service providers to download software updates as new media content and features are introduced.

Technical Details
The Comcerto 100 Series device architecture includes a pair of high-performance ARM 11 processors, a security engine, a 64KB on chip memory, a 128KB L2 cache, plus a 64-bit wide, 165MHz multi-layer interconnect bus and built-in QoS and traffic management capabilities.

The packet-processing ARM core runs standard ANSI C code to enable efficient programming and downloading of algorithms to manage evolving media content delivery requirements. For example, it has the flexibility and performance to run the evolving software for 802.11n routing, deep packet inspection for packet filtering and classification while simultaneously meeting the delay, jitter and latency requirements of real-time services.

The application ARM processor runs all standard operating systems and off-the-shelf applications. All I/O functions are handled automatically via hardware assist with on-chip buffers to reduce bus contention. The use of DDR2 memory lowers total-system cost, and the ability to address 1GB of memory space gives service providers the flexibility and room to deploy the most sophisticated network infrastructure management applications and user services on the platform.

The Comcerto 100 Series also features comprehensive high-performance interfaces for advanced networking and connectivity applications, including dual Gigabit Ethernet interfaces, PCI Bus, USB2.0 with PHY, TDM, and UTOPIA L2 interfaces.

The new family runs OpenWrt Linux distribution and the same field-proven VoIP software suite developed for Mindspeed's Comcerto carrier VoIP processor family. To improve customers' time to market, a complete reference design, including hardware and software is available. Customers also have access to a large number of third-party software and hardware offerings through Mindspeed's OpenMind™ developer's program. The OpenMind program gives customers access to design expertise, proven operating system and software applications, and off-the-shelf and customized hardware, software and system solutions.