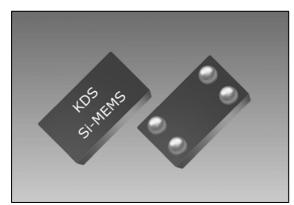
kHz Band MEMS Oscillator



MO1532

Standard Specification



Features

- Fixed 32.768 kHz
- Smallest footprint in chip-scale (CSP): 1.5 x 0.8 mm
- <10 x 10^{-6} frequency tolerance
- ●Ultra-low power: <+1 µA
- Internal filtering eliminates external Vdd bypass cap
- ●NanoDrive[™] programmable output swing for lowest power
- Applications
- Mobile Phones, Tablets
- Health and wellness monitors, Fitness Watches
- Pulse-per- second timekeeping, RTC reference clock
- Battery Management Timekeeping

Min Max Unit Condition Item symbol Тур. Fixed Output Frequency 32.768 Fout kHz $T_A = -10^{\circ}C \text{ to } +70^{\circ}C$ +1.2 _ +3.63 Operating Supply Voltage Vdd V +1.5 +3.63 T_A = -40°C to +85°C °C Operating Temperature Range -10~+70 / -40~+85 T use T_A = -10°C to +70°C, Vdd: +1.5V – +3.63V +75 F_stab +100 x10⁻⁶ T_A = -40°C to +85°C, Vdd: +1.5V – +3.63V Frequency stability [1] $T_A = -10^{\circ}C$ to +70°C, Vdd: +1.2V - +1.5V --+250 +10 T_A = +25°C, post reflow, Vdd: +1.5V - +3.63V x10⁻⁶ Frequency Tolerance [2] F_tol T_A = +25°C, post reflow with board-level underfill, Vdd: +1.5V – +3.63V +20 x10⁻⁶ First yerar Frequency Aging -1.0 +1.0 T_A = +25°C T_A = +25°C, Vdd: +1.8V. No load +0.9 Core Operating Current [3] Idd T_A = -10°C to +70°C, Vdd max: +3.63V. No load +1.3μA -+1.4 Γ_A = -40°C to +85°C, Vdd max: +3.63V. No load 180 $T_{A} = -40^{\circ}C \le T_{A} \le +50^{\circ}C$, valid output 300 Start-up Time at Power-up [4] T_star ms 450 T_A = +50°C < T_A ≤ +85°C, valid output LVCMOS Output Option -40°C to +8 °C, typical values are at T_A = +25°C Output Clock Duty Cycle 48 DC 52 % Vdd: +1.5V - +3.63V, I_{OL} = +10 µA, 15 pF Output Voltage Low Vol Vdd x 0.1 V V_{OH} Output Voltage High Vdd: +1.5V - +3.63V, I_{OH} = -10 µA, 15 pF Vdd x 0.9 V --100 200 10-90% (Vdd), 15 pF load, Vdd = +1.5V to +3.63V Output Rise/Fall Time tr,tf ns 50 10-90% (Vdd), 5 pF load, Vdd ≥ +1.62V NanoDrive™ Programmable, Reduced Swing Output Output Clock Duty Cycle DC 48 52 % MO1532 does not internally AC-couple. This output description is +0.20 to AC-coupled Programmable intended for a receiver that is AC-coupled. V_sw Output Swing +0.80 Vdd: +1.5V – +3.63V, 10 pF Load, I_{OH} / I_{OL} = ±0.2 μA DC-Biased Programmable +0.35 to V_{OL} _ -V Vdd: +1.5V – +3.63V. I_{OL} = +0.2 µA, 10 pF Load. Output Voltage Low Range +0.80 DC-Biased Programmable +0.60 to V_{OH} V Vdd +1.5V – +3.63V. I_{OH} = -0.2 µA, 10 pF Load. -_ Output Voltage High Range +1.225 Output Rise/Fall Time 200 30-70% (V_{OL}/V_{OH}), 10 pF Load tr.tf ns

[1]. Measured peak-to-peak. Inclusive of Initial Tolerance at +25°C, and variations over operating temperature, rated power supply voltage and load.

Stability is specified for two operating voltage ranges. Stability progressively degrades with supply voltage below +1.5V.

[2]. Measured peak-to-peak. Tested with Keysight 53132A frequency counter.

Due to the low operating frequency, the gate time must be ≥100 ms to ensure an accurate frequency measurement.

[3]. Core operating current does not include output driver operating current or load current. To derive total operating current (no load),

add core operating current + (+0.065 μ A/V) * (output voltage swing). [4]. Measured from the time Vdd reaches +1.5V.

Consult our sales representative for other specifications.





MO1532

Dimensions and Patterns

